

SNS COLLEGE OF TECHNOLOGY An Autonomous Institution Coimbatore-35

Accredited by NBA – AICTE and Accredited by NAAC – UGC with 'A++' Grade Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING MICROPROCESSORS AND MICROCONTROLLERS

II YEAR/ IV SEMESTER

UNIT 1 – 8085 AND 8086 ARCHITECTURE

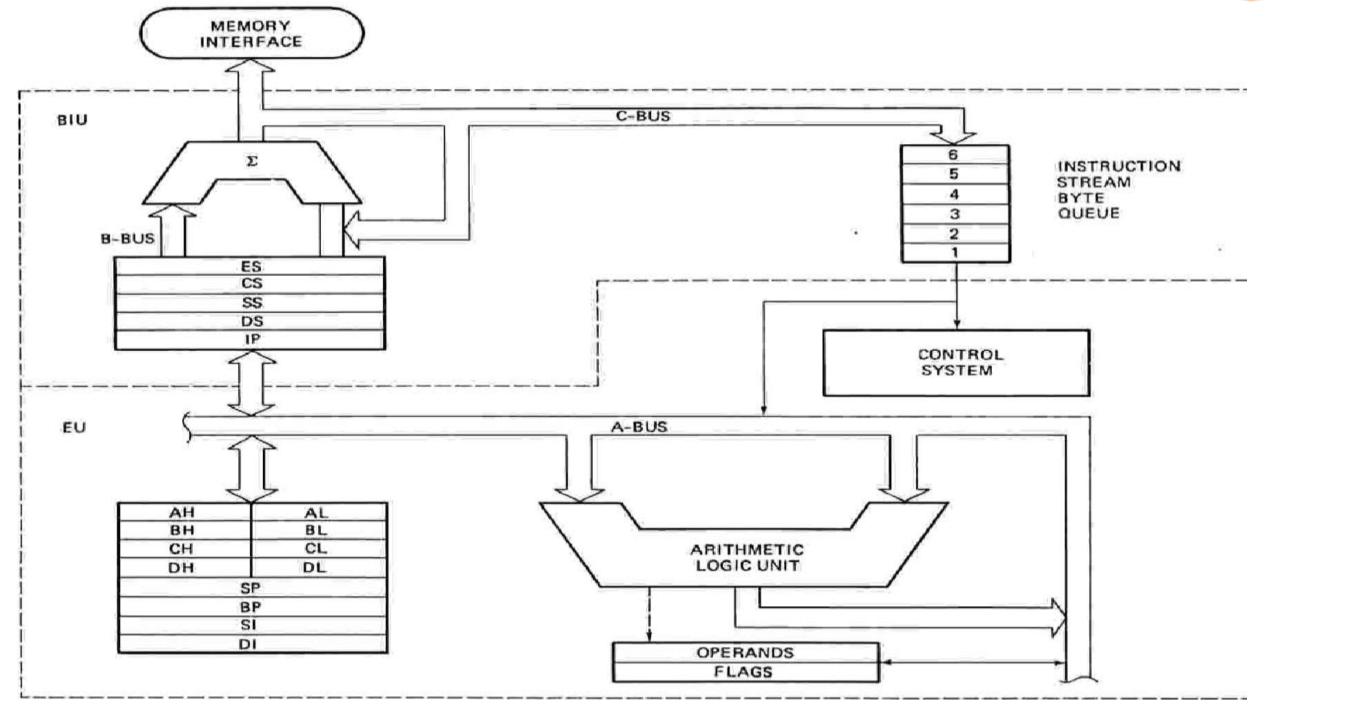
TOPIC – Introduction to 8086 Architecture

8086 ARCHITECTURE/ MICROPROCESSORS & MICROCONTROLLERS/P.UMA MAHESWARI/ECE/SNSCT

3/6/2024







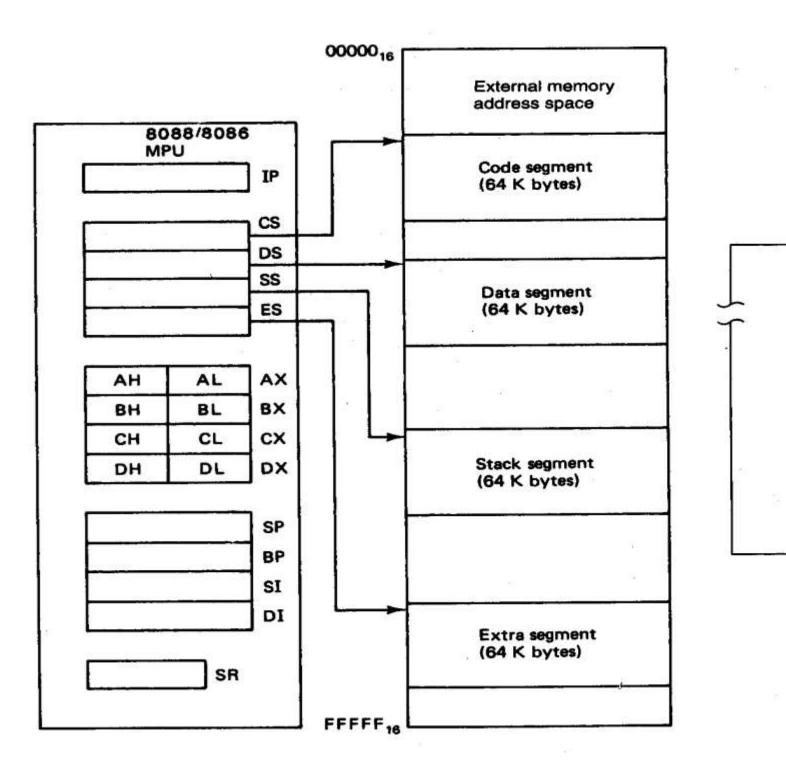
8086 ARCHITECTURE/ MICROPROCESSORS & MICROCONTROLLERS/P.UMA MAHESWARI/ECE/SNSCT

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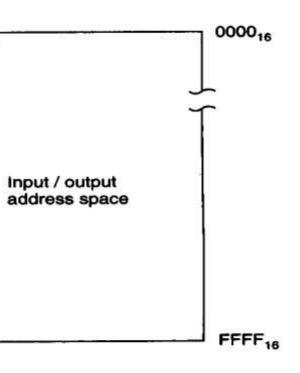




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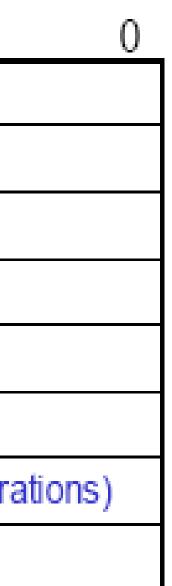


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	AX (Accumulator)				
AH			AL		
	BX (Base Register)				
BH			BL		
CX (Used as a counter)					
CH			CL		
DX (Used to point to data in I/O operation					
DH			DL		

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• AX

- -Accumulator Register
- -Preferred register to use in arithmetic, logic and data transfer instructions because it generates the shortest Machine Language Code
- -Must be used in multiplication and division operations
- -Must also be used in I/O operations

• **BX**

- -Base Register
- -Also serves as an address register







- CX
 - -Count register
 - -Used as a loop counter
 - –Used in shift and rotate operations

• DX

- -Data register
- -Used in multiplication and division
- -Also used in I/O operations







THANK YOU

3/6/2024

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