

SNS COLLEGE OF TECHNOLOGY

(An Autonomous Institution) COIMBATORE-35 Accredited by NBA-AICTE and Accredited by NAAC – UGC with A+ Grade Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai



23EET101 / BASICS OF ELECTRICAL AND ELECTRONICS ENGINEERING I YEAR / I SEMESTER

UNIT-I:AC CIRCUITS Topic:KCL



10.9.23



TOPIC OUTLINE



✓ Introduction✓ KCL✓ Problems







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Introduction







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HISTORY





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LAWS

- A pair of laws stating general restrictions on the current and voltage in an electric circuit
- The first of these states that at any given instant the sum of the voltages around any closed path, or loop, in the network is zero.
- The second states that at any junction of paths, or node, in a network the sum of the currents arriving at any instant is equal to the sum of the currents flowing away.







Introduction to KCL



along with Kirchhoff's Voltage Law makes up the two fundamental laws of Electrical Engineering In this lesson it will be shown how Kirchhoff's Current Law describes the current flow through a junction of a circuit

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KCL



The algebraic sum of all currents entering (+) and leaving (-) any point (junction) in a circuit must equal zero.



Restated as:

The sum of the currents into a junction is equal to the sum of the currents out of that junction.

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- The algebraic sum of all currents entering (+) and leaving (-) any point (junction) in a circuit must equal zero.
- Here, the 3 currents entering the node, I_1 , I_2 , I_3 are all positive in value and the 2 currents leaving the node, I_4 and I_5 are negative in value. Then this means we can also rewrite the equation as;

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$$I_1 + I_2 + I_3 - I_4 - I_5 = 0$$

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...THANK YOU



