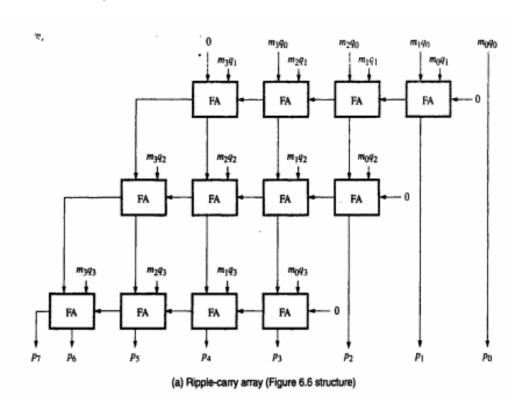
Carry-Save Addition of Summands



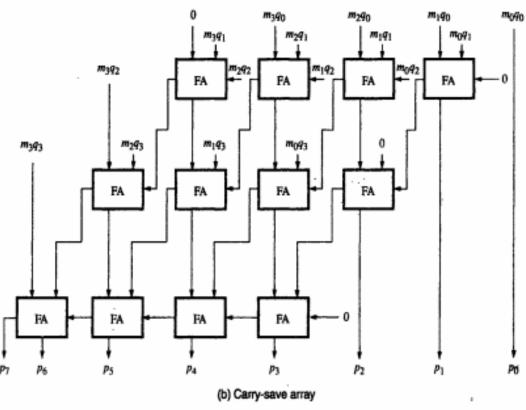


Figure 6.16 Ripple-carry and carry-save arrays for the multiplication operation $M \times Q = P$ for 4-bit operands.

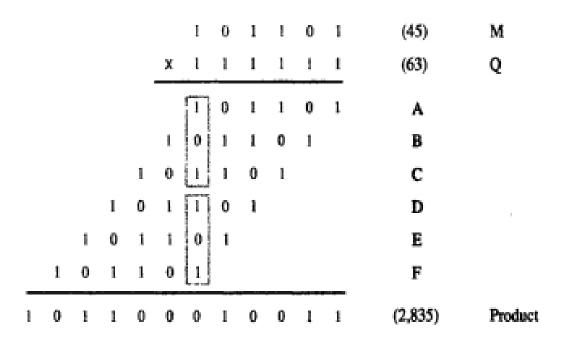


Figure 6.17 A multiplication example used to illustrate carry-save addition as shown in Figure 6.18.

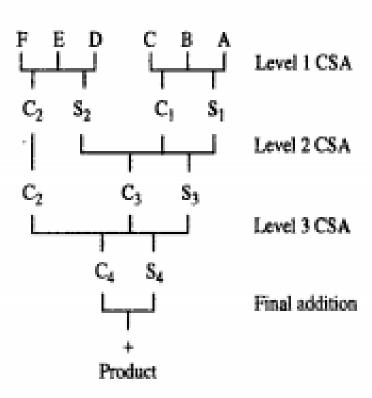


Figure 6.19 Schematic representation of the carry-save addition operations in Figure 6.18.

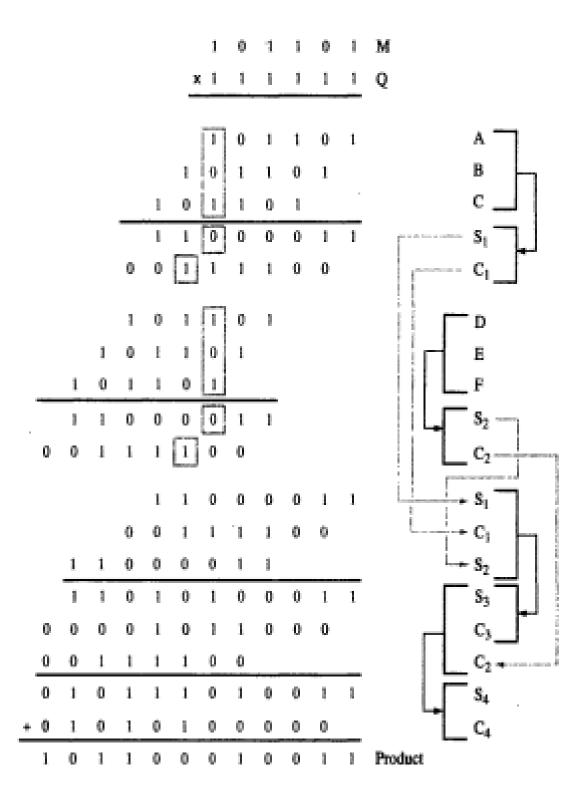


Figure 6.18 The multiplication example from Figure 6.17 performed using carry-save addition.