

Multiple-Bus Organization -

To reduce the No of steps needed, most processors provide multiple internal paths that enable several transfers to take place in parallel.

- * All general purpose registers are combined into single block called Register file.
- * Incrementer eliminates add 4 to PC

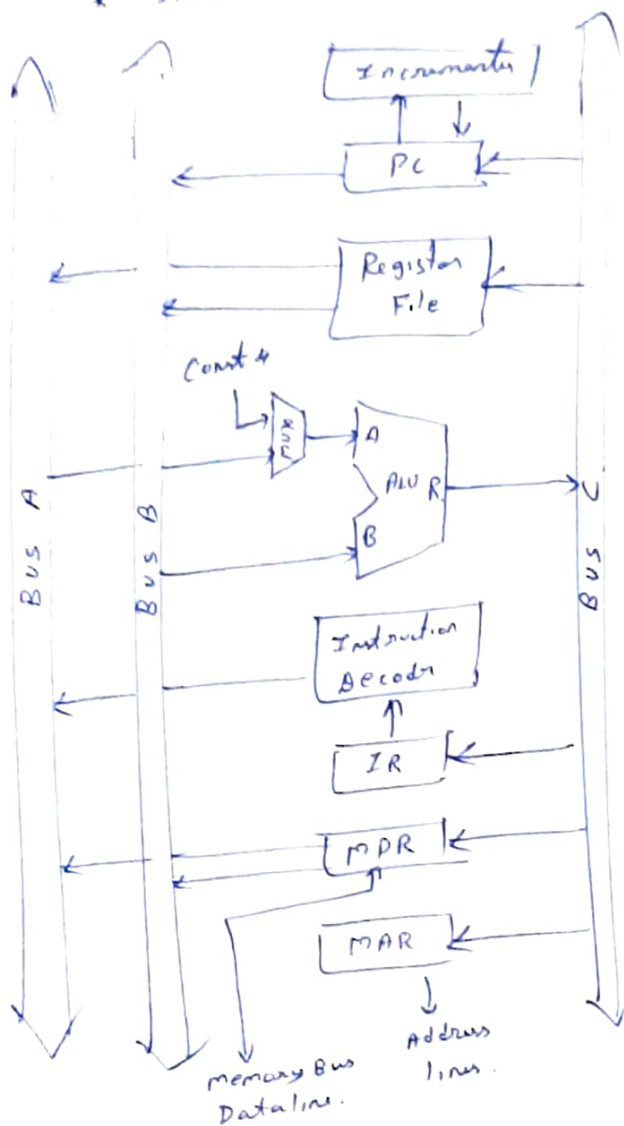


Fig 7-8
Three-Bus Organization of the Datapath.

eg Add R4, R5, R6.

<u>step</u>	<u>Action.</u>
1	PC out, R=B, MARin, Read, Inc PC
2	WMFL
3	MDRoutB, R=B, IRin
4	R4outA, R5outB, Select A, Add, Rbin, End.