

SNS COLLEGE OF TECHNOLOGY

Kurumbapalayam (Po), Coimbatore – 641 107

An Autonomous Institution

Accredited by NBA – AICTE and Accredited by NAAC – UGC with 'A++' Grade Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

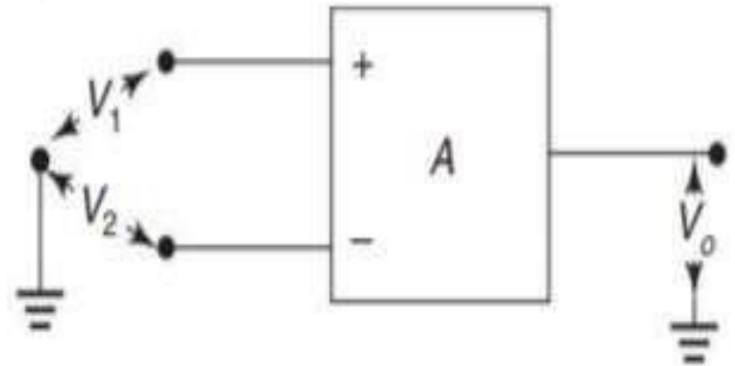
COURSE NAME : 19EC201-ANALOG ELECTRONIC CIRCUITS

Topic : Differential Amplifiers





- The function of a differential amplifier is to amplify the difference between two signals.
- The basic block diagram of a differential amplifier consists of two input \bullet terminals and one output terminal.









The output signal in a differential amplifier is proportional to the difference between the two input signals.

$$V_o = A_d \left(V_1 - V_2 \right)$$

- If V1 = V2, the output voltage is zero.
- A non-zero output voltage is obtained if V1 and V2 are not equal







• The difference-mode input voltage is defined as

$$V_d = (V_1 - V_2)$$

• The common-mode input voltage is defined as

$$V_{\rm cm} = \frac{(V_1 + V_2)}{2}$$





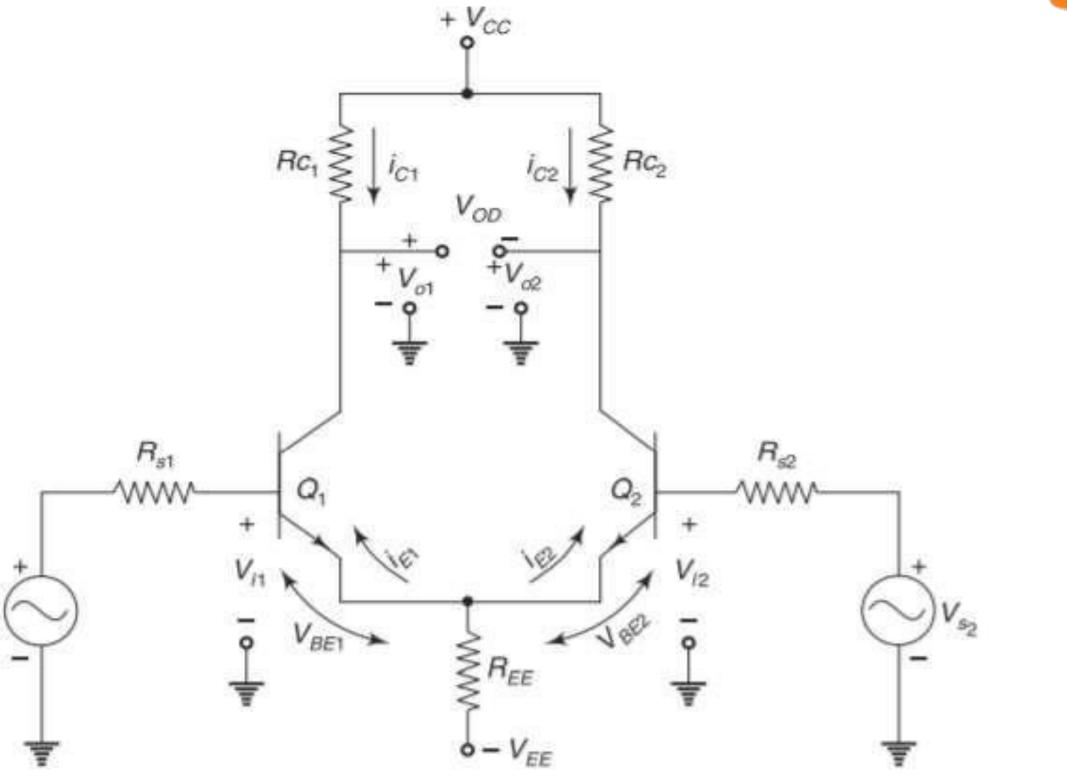
- A differential mode amplifier has two modes of operations \bullet
- They are \bullet
- Differential mode operation
- Common mode operation





Differential Mode Operation





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Differential Mode Operation

Vout & (VI-V2



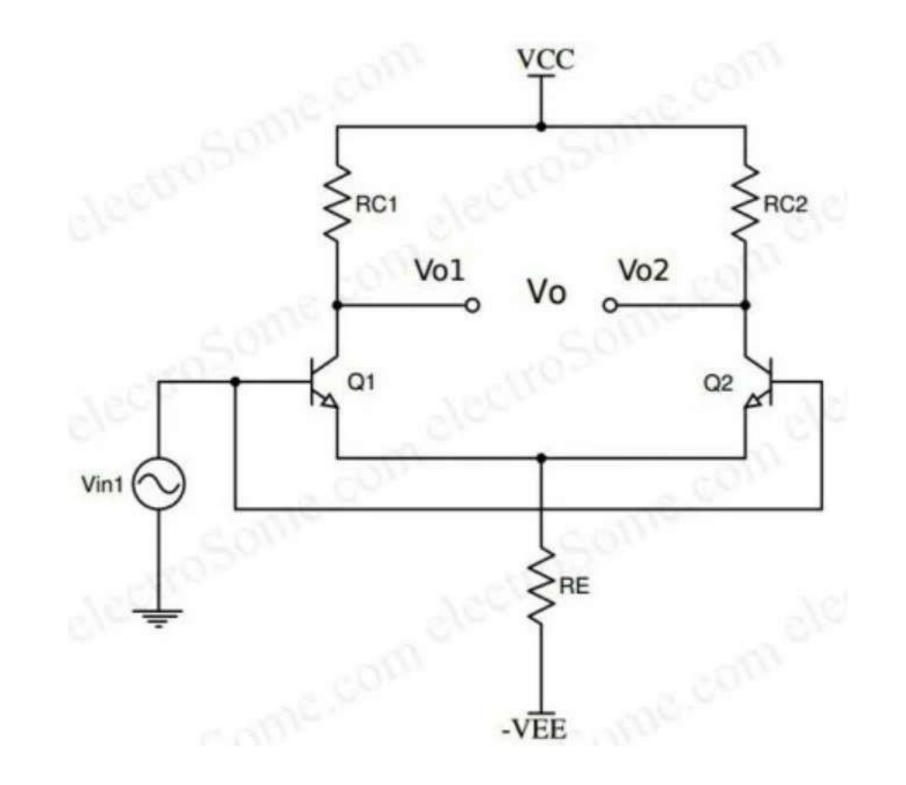


Vd = V1 - V2

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Common Mode Operation

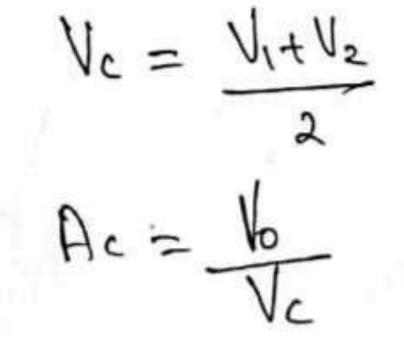






Common Mode Operation

A differential amplifier is said to be in common mode when same signal is applied to both inputs and the expected output will be zero, ie ideally common mode gain is zero.







Common Mode Rejection Ratio (CMRR)

- •CMRR is introduced to define the ability of a differential amplifier to reject common mode signal.
- •CMRR is defined as the ratio of the differential voltage gain Ad to common mode
- gain Ac and is generally expressed in dB.

$$CMRR = 20 \log_{10} \left| \frac{A_d}{A_c} \right|$$









Features of Differential Amplifier

- High differential voltage gain
- Low common mode gain
- High CMRR
- High Input impedance
- Large bandwidth
- Low output impedance





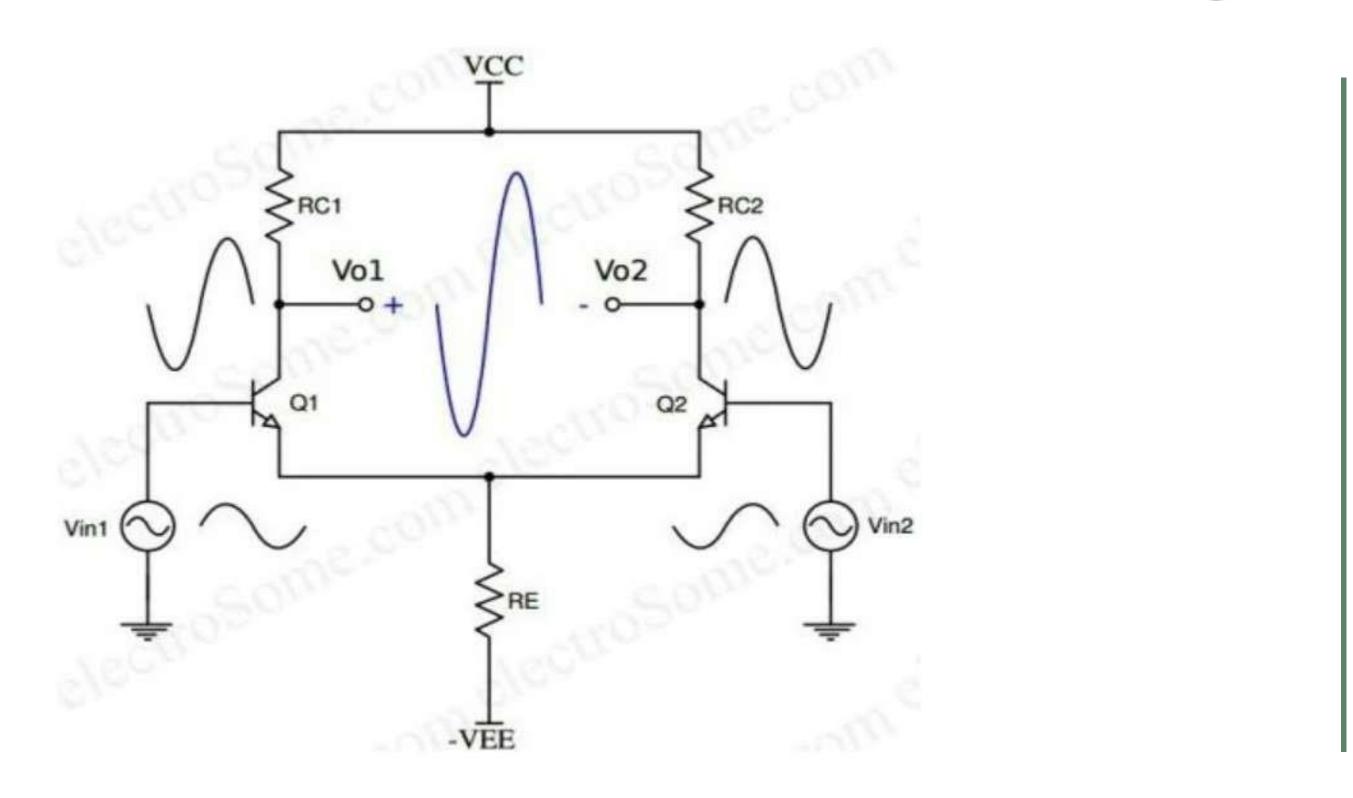
- Dual input, balanced output differential amplifier
- Dual input, unbalanced output differential amplifier \bullet
- Single input balanced output differential amplifier
- Single input unbalanced output differential amplifier





Dual input, balanced output differential amplifier

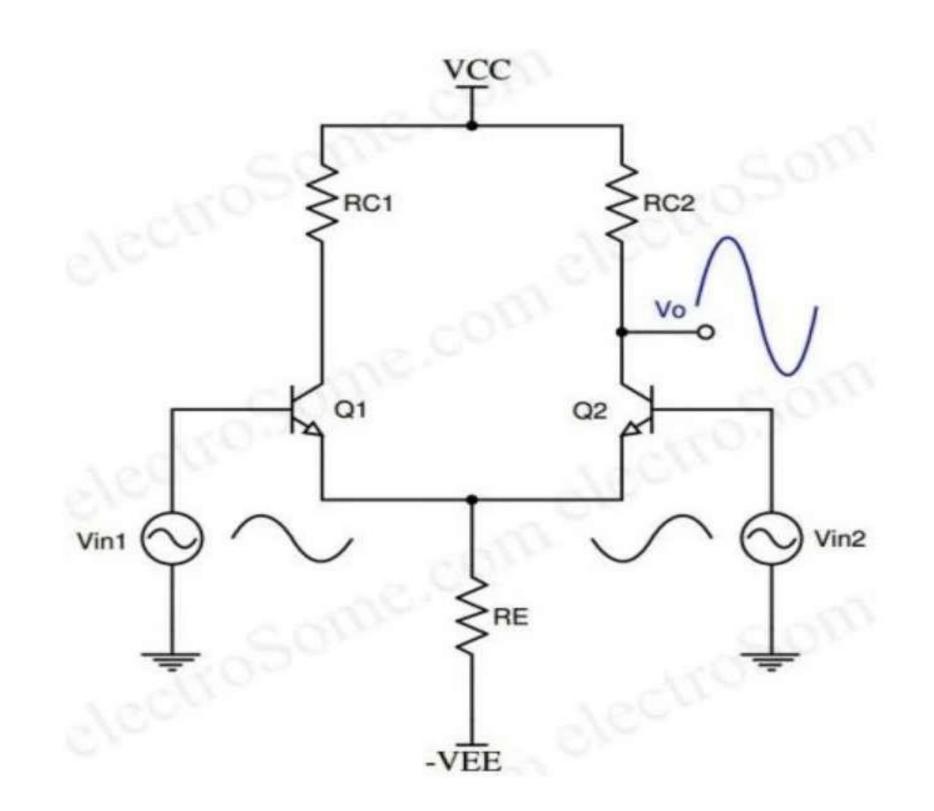








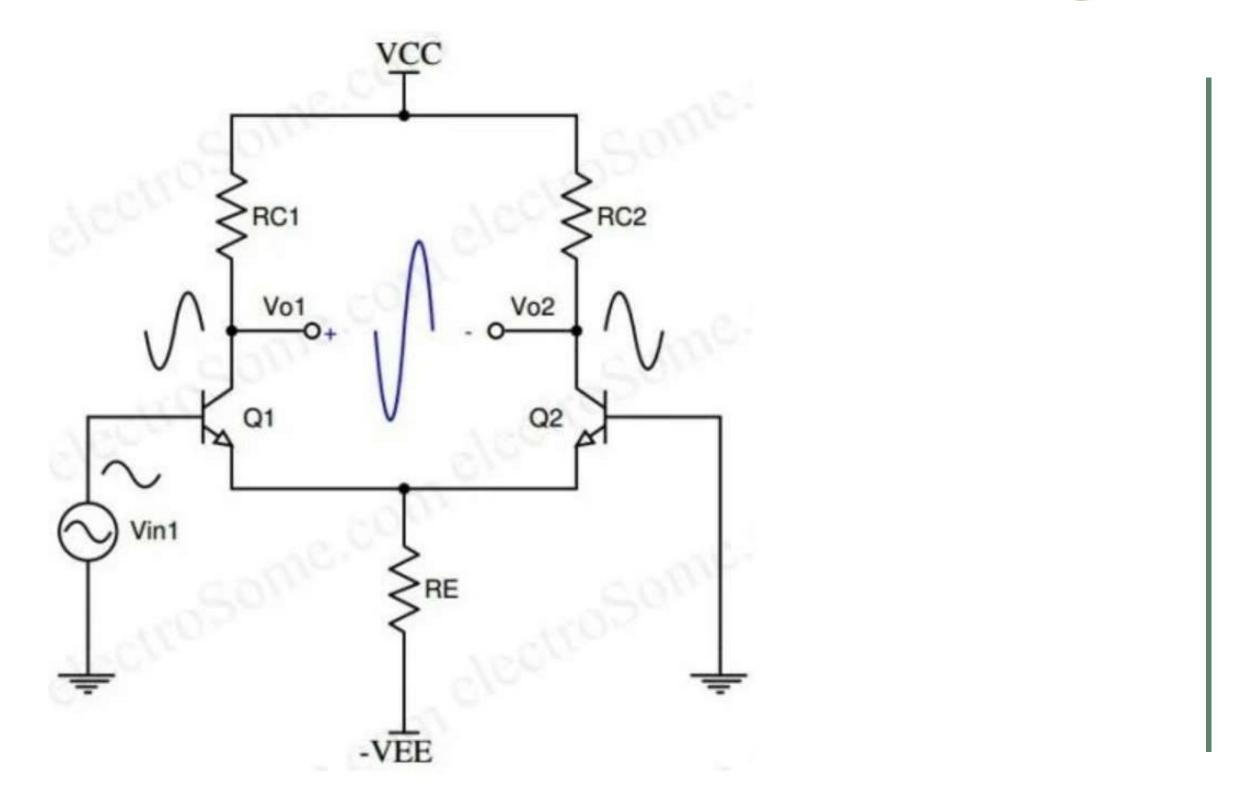
Dual input, unbalanced output differential amplifier





Single input balanced output differential amplifier

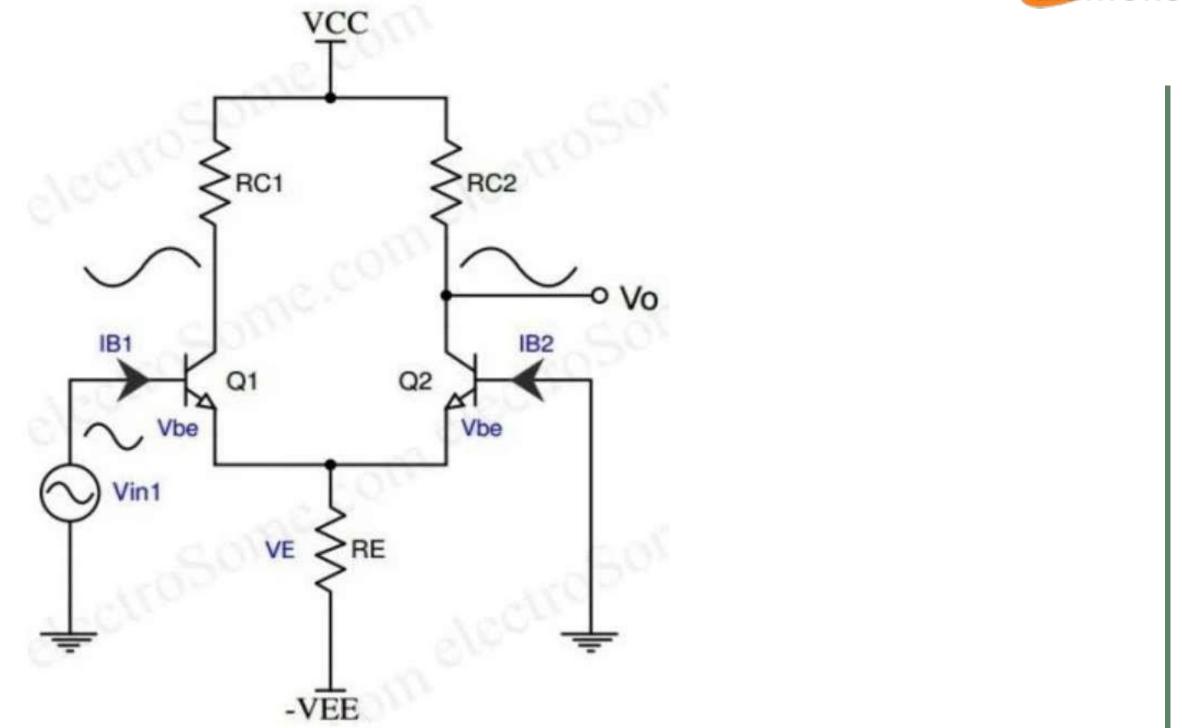






Single input unbalanced output differential amplifier









Single input unbalanced output differential amplifier

THANK YOU

DIFFERENTIAL AMPLIFIER/19ECB201-ANALOG ELECTRONIC CIRCUITS/RAJA S AP/ECE/SNSCT



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