



# **SNS COLLEGE OF TECHNOLOGY**

**Coimbatore-35**  
**An Autonomous Institution**

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## **DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

### **19ECB211 – MICROCONTROLLER PROGRAMMING & INTERFACING**

**II YEAR IV SEM**

**UNIT I – PIC MICROCONTROLLER : HISTORY , FEATURES & ARCHITECTURE**

**TOPIC 8 – Program Counter & Program ROM Space in PIC**



## Program Counter in PIC

- The role of the Program Counter (PC) in executing a program and show how the code is fetched from ROM and executed.
- Program Counter PC is the important register in the PIC microcontroller.
- The program counter is used by the CPU to point to the address of the next instruction to be executed.



## Program Counter in PIC

- As the CPU fetches the opcode from the program ROM, the PC is incremented automatically to point to the next instruction.
- The wider the program counter, more the memory locations a CPU can access.
- A 14-bit PC can access a maximum of 16K,  $2^{14} = 16K$ , of code from address 0000 - 3FFFH.
- The PIC family 16F has 14-bit program counters.



## Program Counter & Program ROM Space in PIC

- The program counter in PIC12F is 12-bit. In the case of a 16-bit program counter, the code space is 64K,  $2_{16} = 64K$ , which occupies the 0000 - FFFFH address range.
- The 8051 micro-controllers have a 16-bit program counter.
- The program counter in the PIC18 family is 21-bit. This means that the PIC18 can access, a total of 2M of code.
- Not all members of the PIC family have the entire 2M,  $2^{21} = 2M$ , of on-chip ROM installed.

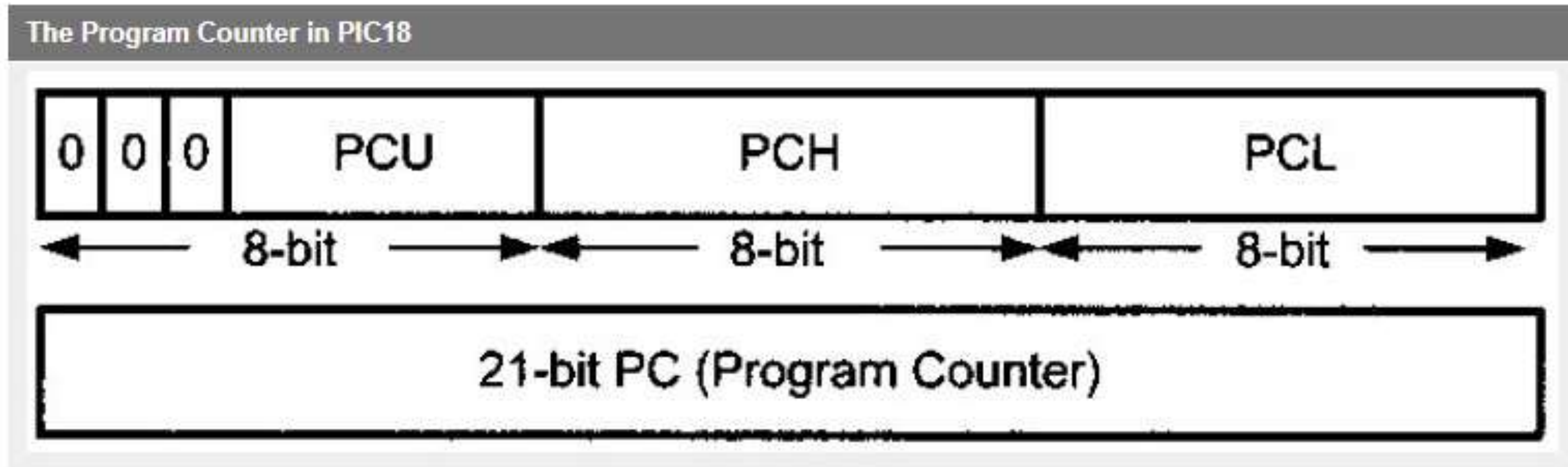


## Program Counter in PIC

- The 14-bit program counter in the PIC16C family had imposed the maximum code size of 16K.
- To overcome this major limitation, PIC designers had to introduce the use of page switching in the later members of the PIC16 family.



# Program Counter in PIC





# References



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*Thank You*