Booth Algorithm

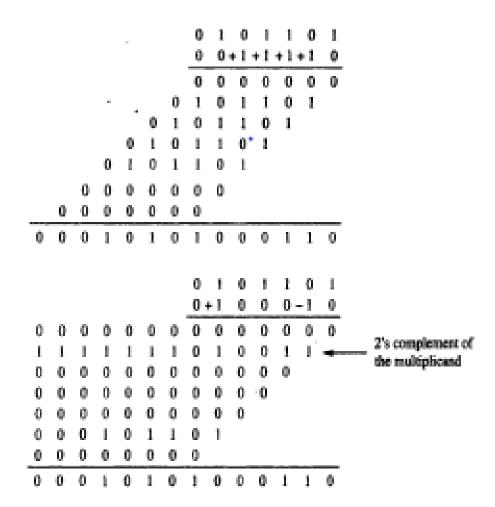


Figure 6.9 Normal and Booth multiplication schemes.

Mult	iplier	Version of multiplicand
Bit i	Bit $i-1$	selected by bit i
0	0	0×M
0	1	+1×M
1	0	-1×M
1	1	0×M

Figure 6.12 Booth multiplier recoding table.

Figure 6.10 Booth recoding of a multiplier.

Figure 6.11 Booth multiplication with a negative multiplier.