



SNS COLLEGE OF TECHNOLOGY

Coimbatore-35
An Autonomous Institution



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

19EET301 / POWER ELECTRONICS AND DRIVES

V SEM EEE

UNIT 3 –AC CONVERTERS

MULTI STAGE SEQUENCE CONTROL

Dr. R SENTHIL KUMAR

ASP/EEE, SNSCT



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MULTISTAGE SEQUENCE CONTROL

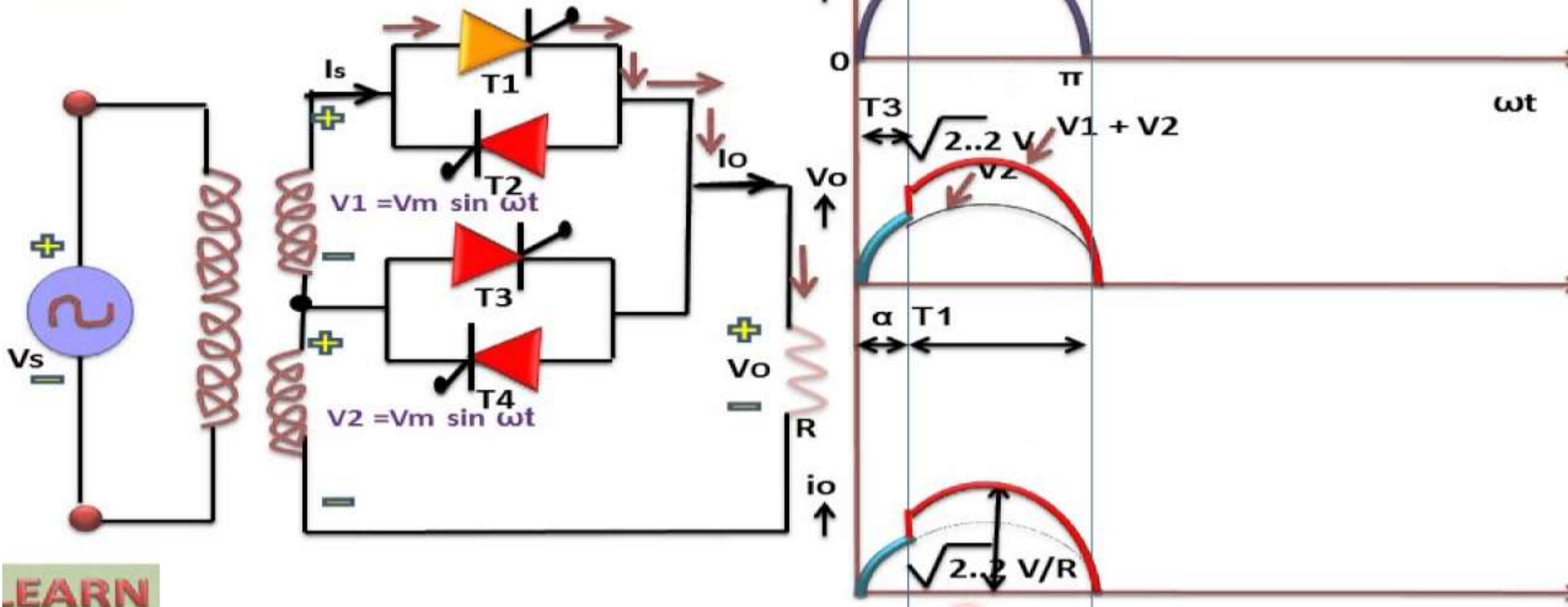
Sequence control of ac regulators means the use of two or more stages of voltage controllers in parallel for the **regulation of output voltage**.

The sequence control of ac voltage controllers can be used as voltage controllers in **supply systems** & for the **speed control of induction motors**. These types of controllers are known as **synchronous tap changers** or **transformer tap changers**.



MULTISTAGE SEQUENCE CONTROL

GROW

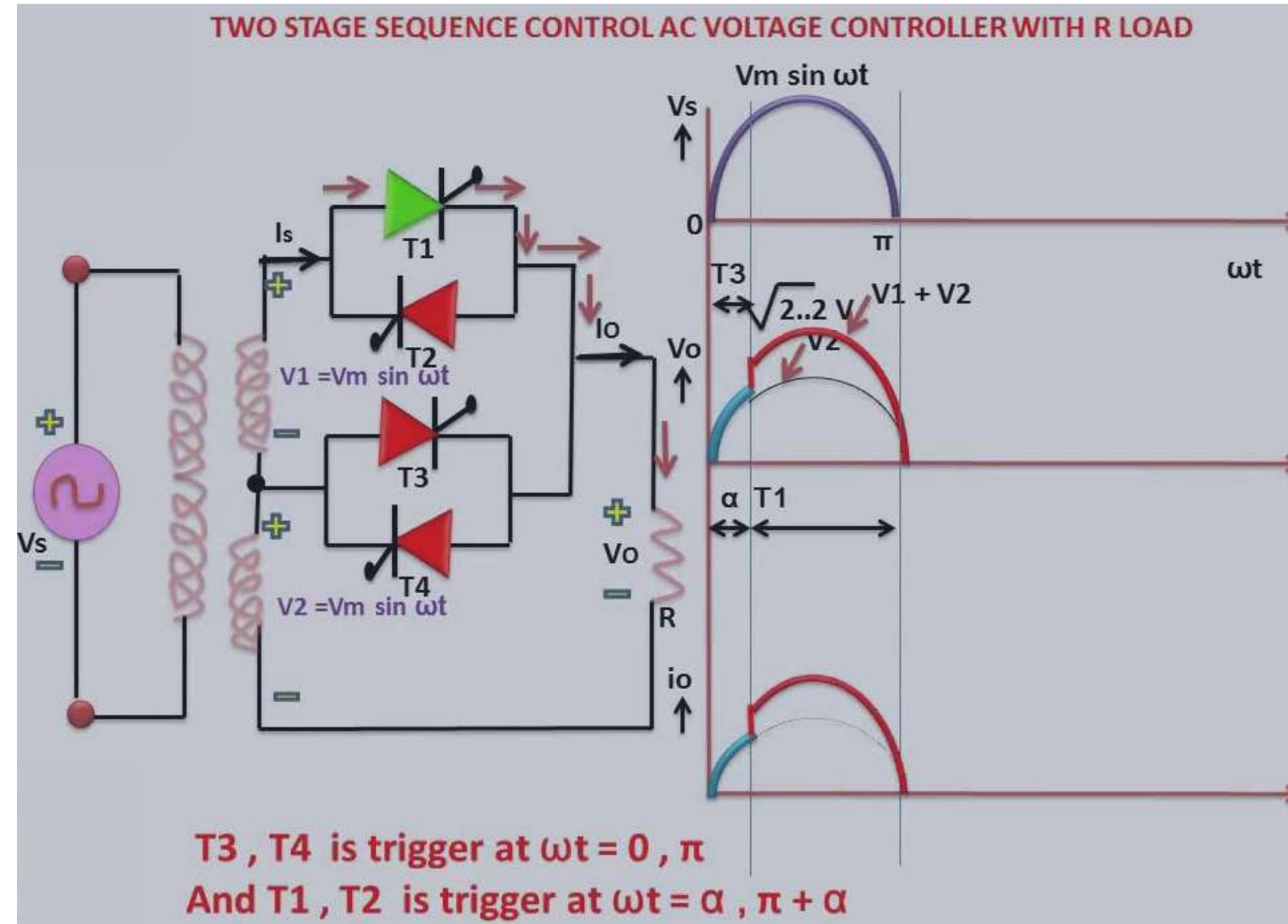


LEARN



MULTISTAGE SEQUENCE CONTROL

- Thyristors are used as **static switches** for on load changing of transformer connections.
- The turns ratio of the input transformer are such that if the primary instantaneous voltage is shown
 $V_s = V_m \sin \omega t$
- Secondary instantaneous voltages are,
 $V_1 = V_m \sin \omega t$
 $V_2 = V_m \sin \omega t$
- When thyristors T3 & T4 are alternately fired with delay angle of $\alpha=0$, the load voltage is $V_o = V_1$.
- If full output voltage is required, thyristors T1 & T2 are alternately fired with delay angle of $\alpha=0$ and full voltage $V_o = V_1 + V_2$.





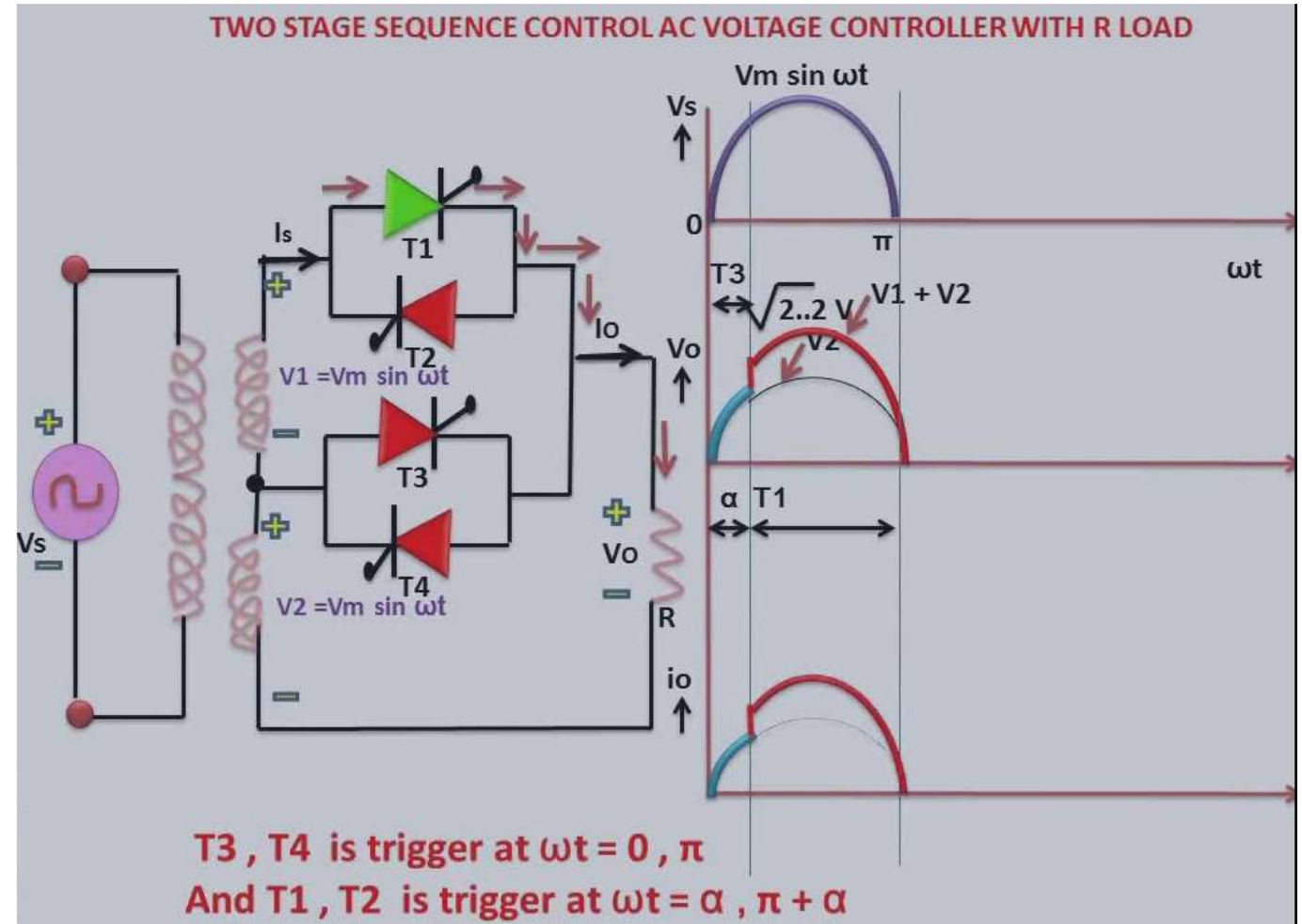
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MULTISTAGE SEQUENCE CONTROL

The gating pulse of thyristors can be controlled to vary the load voltage. The RMS value of load voltage V_o can be varied within three possible ranges.

- $0 < V_o < V_1$,
- $0 < V_o < (V_1 + V_2)$,
- $V_1 < V_o < (V_1 + V_2)$





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MULTISTAGE SEQUENCE CONTROL

Case1: $0 < V_o < V_1$

To vary the RMS voltage within this range, T1 & T2 are turned off. T3 & T4 can be operated as a single phase ac voltage regulator. The RMS load voltage is given by,

$V_o = V_1 [1/\pi(\pi - \alpha + (\sin 2\alpha/2))]^{1/2}$ and the firing angle range is $0 < \alpha < \pi$.

Case2: $0 < V_o < (V_1 + V_2)$

T3 & T4 are turned off. T1 & T2 operate as a single phase ac voltage regulator, the load voltage is

$V_o = (V_1 + V_2) [1/\pi(\pi - \alpha + (\sin 2\alpha/2))]^{1/2}$.



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MULTISTAGE SEQUENCE CONTROL

Case3: $V_1 < V_o < (V_1 + V_2)$

T3 is turned on at $\omega t = 0$ and the secondary voltage V_1 appears across the load. If T1 is turned on at $\omega t = \alpha$, T3 is reverse biased due to secondary voltage V_2 & T3 is turned off. The voltage across the load is $(V_1 + V_2)$. At $\omega t = \pi$, T1 is self commutated & T4 is turned on. The secondary voltage V_1 appears across the load until T1 is fired $\omega t = \pi + \alpha$, T4 is turned off due to reverse voltage V_2 and the load voltage is $(V_1 + V_2)$. At $\omega t = 2\pi$, T2 is self commutated, T3 is turned on again the cycle is repeated. This type of controller is also called as synchronous tap changer

THANK YOU