### Study Guide for Operating Systems

#### **Unit.1 - Introduction**

- An OS is a program that acts as an intermediary between a user of a computer and the computer hardware
- Goals: Execute user programs, make the comp. system easy to use, utilize hardware efficiently
- Computer system: Hardware  $\leftrightarrow$  OS  $\leftrightarrow$  Applications  $\leftrightarrow$  Users ( $\leftrightarrow$  = 'uses')
- OS is:
  - Resource allocator: decides between conflicting requests for efficient and fair resource use
  - Control program: controls execution of programs to prevent errors and improper use of computer
- <u>Kernel:</u> the one program running at all times on the computer
- <u>Bootstrap program:</u> loaded at power-up or reboot
  - Stored in ROM or EPROM (known as <u>firmware</u>), Initializes all aspects of system, loads OS kernel and starts execution
- I/O and CPU can execute concurrently
- Device controllers inform CPU that it is finished w/ operation by causing an <u>interrupt</u>
  - Interrupt transfers control to the interrupt service routine generally, through the <u>interrupt vector</u>, which contains the addresses of all the service routines
  - Incoming interrupts are disabled while another interrupt is being processed
  - <u>Trap</u> is a software generated interrupt caused by error or user request
  - OS determines which type of interrupt has occurred by polling or the vectored interrupt system
- <u>System call:</u> request to the operating system to allow user to wait for I/O completion
- <u>Device-status table:</u> contains entry for each I/O device indicating its type, address, and state
  - OS indexes into the I/O device table to determine device status and to modify the table entry to include interrupt
- Storage structure:
  - Main memory <u>random access</u>, <u>volatile</u>
  - Secondary storage extension of main memory That provides large non-volatile storage
  - Disk divided into <u>tracks</u> which are subdivided into <u>sectors</u>. <u>Disk controller</u> determines logical interaction between the device and the computer.
- <u>Caching</u> copying information into faster storage system
- <u>Multiprocessor Systems:</u> Increased throughput, economy of scale, increased reliability
  - Can be asymmetric or symmetric
  - <u>Clustered systems</u> Linked multiprocessor systems
- <u>Multiprogramming</u> Provides efficiency via job scheduling
  - When OS has to wait (ex: for I/O), switches to another job
- <u>Timesharing</u> CPU switches jobs so frequently that each user can interact with each job while it is running (<u>interactive computing</u>)
- <u>Dual-mode</u> operation allows OS to protect itself and other system components <u>User mode</u> and <u>kernel mode</u>
   Some instructions are only executable in kernel mode, these are <u>privileged</u>
- Single-threaded processes have one program counter, multi-threaded processes have one PC per thread
- <u>Protection</u> mechanism for controlling access of processes or users to resources defined by the OS
- Security defense of a system against attacks
- User IDs (UID), one per user, and Group IDs, determine which users and groups of users have which privileges



### **OS Structures**

- <u>User Interface (UI)</u> Can be <u>Command-Line (CLI)</u> or <u>Graphics User Interface (GUI)</u> or <u>Batch</u>
  - These allow for the user to interact with the system services via system calls (typically written in C/C++)
- Other system services that a helpful to the <u>user</u> include: program execution, I/O operations, file-system manipulation, communications, and error detection
- Services that exist to ensure efficient OS operation are: resource allocation, accounting, protection and security
- Most system calls are accessed by Application Program Interface (API) such as Win32, POSIX, Java
- Usually there is a number associated with each system call
  - System call interface maintains a table indexed according to these numbers
- Parameters may need to be passed to the OS during a system call, may be done by:
  - Passing in <u>registers</u>, address of parameter stored in a <u>block</u>, <u>pushed</u> onto the stack by the program and <u>popped</u> off by the OS
  - Block and stack methods do not limit the number or length of parameters being passed
- <u>Process control</u> system calls include: end, abort, load, execute, create/terminate process, wait, allocate/free memory
- <u>File management</u> system calls include: create/delete file, open/close file, read, write, get/set attributes
- <u>Device management</u> system calls: request/release device, read, write, logically attach/detach devices
- <u>Information maintenance</u> system calls: get/set time, get/set system data, get/set process/file/device attributes
- <u>Communications</u> system calls: create/delete communication connection, send/receive, transfer status information

- OS <u>Layered</u> approach:
  - The operating system is divided into a number of layers (levels), each built on top of lower layers. The bottom layer (layer 0), is the hardware; the highest (layer N) is the user interface
  - With modularity, layers are selected such that each uses functions (operations) and services of only lower-level layers
- <u>Virtual machine</u>: uses layered approach, treats hardware and the OS kernel as though they were all hardware.
  - Host creates the illusion that a process has its own processor and own virtual memory
  - Each guest provided with a 'virtual' copy of the underlying computer
- Application failures can generate core dump file capturing memory of the process
- Operating system failure can generate <u>crash dump</u> file containing kernel memory

#### Processes

- <u>Process</u> contains a program counter, stack, and data section.
  - <u>Text section</u>: program code itself
  - <u>Stack</u>: temporary data (function parameters, return addresses, local variables)
  - <u>Data section</u>: global variables
  - <u>Heap</u>: contains memory dynamically allocated during run-time
- <u>Process Control Block (PCB)</u>: contains information associated with each process: process state, PC, CPU registers, scheduling information, accounting information, I/O status information
- Types of processes:
  - <u>I/O Bound</u>: spends more time doing I/O than computations, many short CPU bursts
  - <u>CPU Bound</u>: spends more time doing computations, few very long CPU bursts
- When CPU switches to another process, the system must save the state of the old process (to PCB) and load the saved state (from PCB) for the new process via a <u>context switch</u>
  - Time of a context switch is dependent on hardware
  - Parent processes create children processes (form a tree)
    - <u>PID</u> allows for process management
    - Parents and children can share all/some/none resources
    - Parents can execute concurrently with children or wait until children terminate
    - <u>fork()</u> system call creates new process
      - <u>exec()</u> system call used after a fork to replace the processes' memory space with a new program
- Cooperating processes need interprocess communication (IPC): shared memory or message passing
- <u>Message passing</u> may be blocking or non-blocking
  - <u>Blocking</u> is considered <u>synchronous</u>
    - Blocking send has the sender block until the message is received
    - Blocking receive has the receiver block until a message is available
  - <u>Non-blocking</u> is considered <u>asynchronous</u>
    - Non-blocking send has the sender send the message and continue
    - <u>Non-blocking receive</u> has the receiver receive a valid message or null





# Unit 2 – Threads

- Threads are fundamental unit of CPU utilization that forms the basis of multi-threaded computer systems
- · Process creation is heavy-weight while thread creation is light-weight
  - Can simplify code and increase efficiency
- Kernels are generally multi-threaded
- <u>Multi-threading</u> models include: Many-to-One, One-to-One, Many-to-Many
  - <u>Many-to-One</u>: Many user-level threads mapped to single kernel thread
  - <u>One-to-One</u>: Each user-level thread maps to kernel thread
  - <u>Many-to-Many</u>: Many user-level threads mapped to many kernel threads
- Thread library provides programmer with API for creating and managing threads
- Issues include: thread cancellation, signal handling (synchronous/asynchronous), handling thread-specific data, and scheduler activations.
  - <u>Cancellation</u>:
    - Asynchronous cancellation terminates the target thread immediately
    - Deferred cancellation allows the target thread to periodically check if it should be canceled
  - Signal handler processes signals generated by a particular event, delivered to a process, handled
  - <u>Scheduler</u> activations provide <u>upcalls</u> a communication mechanism from the kernel to the thread library.
    - Allows application to maintain the correct number of kernel threads

### **CPU Scheduling**

- Process execution consists of a cycle of CPU execution and I/O wait
- CPU scheduling decisions take place when a process:
  - 0 Switches from running to waiting (nonpreemptive)
  - Switches from running to ready (preemptive) 0
  - 0 Switches from waiting to ready (preemptive)
  - Terminates (nonpreemptive) 0
- The dispatcher module gives control of the CPU to the process selected by the short-term scheduler
  - Dispatch latency- the time it takes for the dispatcher to stop one process and start another 0
- Scheduling algorithms are chosen based on optimization criteria (ex: throughput, turnaround time, etc.)
  - FCFS, SJF, Shortest-Remaining-Time-First (preemptive SJF), Round Robin, Priority 0
- Determining length of next CPU burst: Exponential Averaging:
  - $t_n = actual length of n<sup>th</sup> CPU burst$ 1.
  - $\tau_{n+1}$  = predicted value for the next CPU burst 2.
  - 3.  $\alpha, 0 \le \alpha \le 1$  (commonly  $\alpha$  set to 1/2)
  - Define:  $\tau_{n+1} = \alpha^* t_n + (1-\alpha)\tau_n$ 4.
- Priority Scheduling can result in starvation, which can be solved by aging a process (as time progresses, increase the priority)
- In Round Robin, small time quantums can result in large amounts of context switches
  - Time quantum should be chosen so that 80% of processes have 0 shorter burst times that the time quantum
- Multilevel Queues and Multilevel Feedback Queues have multiple process queues that have different priority levels
- 12 τ, 10 t, 6 4 2 time 6 4 6 4 13 13 13 8 6 6 5 9 11 12
- 0 In the Feedback queue, priority is not fixed  $\rightarrow$  Processes can be promoted and demoted to different queues
- 0 Feedback queues can have different scheduling algorithms at different levels
- Multiprocessor Scheduling is done in several different ways:
  - <u>Asymmetric multiprocessing</u>: only one processor accesses system data structures  $\rightarrow$  no need to data share 0
  - Symmetric multiprocessing: each processor is self-scheduling (currently the most common method) 0
  - Processor affinity: a process running on one processor is more likely to continue to run on the same processor 0 (so that the processor's memory still contains data specific to that specific process)
- Little's Formula can help determine average wait time per process in any scheduling algorithm:
  - $n = \lambda x W$ 0
  - n = avg queue length; W = avg waiting time in queue;  $\lambda$  = average arrival rate into queue 0
- Simulations are programmed models of a computer system with variable clocks
  - Used to gather statistics indicating algorithm performance 0
  - Running simulations is more accurate than queuing models (like Little's Law) 0

Although more accurate, high cost and high risk



#### **Unit 3 – Process Synchronization**

- <u>Race Condition</u>: several processes access and manipulate the same data concurrently, outcome depends on which order each access takes place.
- Each process has critical section of code, where it is manipulating data
  - To solve critical section <u>problem</u> each process must ask permission to enter critical section in <u>entry section</u>, follow critical section with <u>exit section</u> and then execute the <u>remainder section</u>
  - Especially difficult to solve this problem in preemptive kernels
- Peterson's Solution: solution for two processes
  - Two processes share two variables: int turn and Boolean flag[2]
  - **turn:** whose turn it is to enter the critical section
  - flag: indication of whether or not a process is ready to enter critical section
    - flag[i] = true indicates that process P<sub>i</sub> is ready
  - Algorithm for process P<sub>i</sub>:

do {

```
flag[i] = TRUE;
turn = j;
while (flag[j] && turn == j)
critical section
flag[i] = FALSE;
remainder section
```

} while (TRUE);

- Modern machines provide atomic hardware instructions: <u>Atomic</u> = non-interruptable
- Solution using Locks:

do {

acquire lock critical section release lock

remainder section

```
} while (TRUE);
```

• Solution using <u>Test-And-Set</u>: Shared boolean variable lock, initialized to FALSE



• Solution using Swap: Shared bool variable lock initialized to FALSE; Each process has local bool variable key

```
void Swap (boolean *a, boolean *b){
    boolean temp = *a;
    *a = *b;
    *b = temp:
}

do {
    key = TRUE;
    while ( key == TRUE)
    Swap (&lock,
    &key );
    // critical section
    lock = FALSE;
    // remainder section
} while (TRUE);
```

- <u>Semaphore</u>: Synchronization tool that does not require busy waiting
  - Standard operations: wait() and signal()  $\leftarrow$  these are the only operations that can access semaphore S
  - Can have <u>counting</u> (unrestricted range) and <u>binary</u> (0 or 1) semaphores
- <u>Deadlock</u>: Two or more processes are waiting indefinitely for an event that can be caused by only one of the waiting processes (most OSes do not prevent or deal with deadlocks)
  - Can cause <u>starvation</u> and <u>priority inversion</u> (lower priority process holds lock needed by higher-priority process)

### Deadlocks

- <u>Deadlock Characteristics</u>: deadlock can occur if these conditions hold simultaneously
  - <u>Mutual Exclusion</u>: only one process at a time can use a resource
  - <u>Hold and Wait</u>: process holding one resource is waiting to acquire resource held by another process
  - No Preemption: a resource can be released only be the process holding it after the process completed its task
  - $_{\circ}$  <u>Circular Wait</u>: set of waiting processes such that  $P_{n-1}$  is waiting for resource from  $P_n$ , and  $P_n$  is waiting for  $P_0$ 
    - "Dining Philosophers" in deadlock

#### **Unit 4 – Memory Management**

- Cache sits between main memory and CPU registers
- Base and limit registers define logical address space usable by a process
- Compiled code addresses <u>bind</u> to relocatable addresses
  - Can happen at three different stages
    - <u>Compile time</u>: If memory location known a priori, <u>absolute code</u> can be generated
    - Load time: Must generate relocatable code if memory location not known at compile time
    - Execution time: Binding delayed until run time if the process can be moved during its execution
- <u>Memory-Management Unit (MMU)</u> device that maps virtual to physical address
- Simple scheme uses a relocation register which just adds a base value to address
- <u>Swapping</u> allows total physical memory space of processes to exceed physical memory
  - Def: process swapped out temporarily to backing store then brought back in for continued execution
- <u>Backing store</u>: fast disk large enough to accommodate copes of all memory images
- <u>Roll out, roll in</u>: swapping variant for priority-based scheduling.
  - Lower priority process swapped out so that higher priority process can be loaded
- Solutions to Dynamic Storage-Allocation Problem:

-

- <u>First-fit:</u> allocate the first hole that is big enough
- <u>Best-fit</u>: allocate the smallest hole that is big enough (must search entire list)  $\rightarrow$  smallest leftover hole
- Worst-fit: allocate the largest hole (search entire list)  $\rightarrow$  largest leftover hole
- External Fragmentation: total memory space exists to satisfy request, but is not contiguous
  - Reduced by compaction: relocate free memory to be together in one block
    - Only possible if relocation is dynamic
- Internal Fragmentation: allocated memory may be slightly larger than requested memory
- Physical memory divided into fixed-sized frames: size is power of 2, between 512 bytes and 16 MB
- Logical memory divided into same sized blocks: pages
- Page table used to translate logical to physical addresses
  - Page number (p): used as an index into a page table
- Page offset (d): combined with base address to define the physical memory address
- Free-frame list is maintained to keep track of which frames can be allocated

page number	page offset
p	d
<i>m</i> - <i>p</i>	n

For given logical address space 2<sup>m</sup> and page size 2<sup>n</sup>



# **Main Memory Continued**

- Transition Look-aside Buffer (TLB) is a CPU cache that memory management hardware uses to improve virtual address translation speed
  - Typically small 64 to 1024 entries 0
  - 0 On TLB miss, value loaded to TLB for faster access next time
  - 0 TLB is associative - searched in parallel



Paging with TLB



- Effective Access Time: EAT =  $(1 + \varepsilon) \alpha + (2 + \varepsilon)(1 \alpha)$ 
  - 0  $\varepsilon$  = time unit,  $\alpha$  = hit ratio
- Valid and invalid bits can be used to protect memory
  - 0 "Valid" if the associated page is in the process' logical address space, so it is a legal page
- Can have multilevel page tables (paged page tables)
- Hashed Page Tables: virtual page number hashed into page table
  - 0 Page table has chain of elements hashing to the same location
  - Each element has (1) virtual page number, (2) value of mapped page frame, (3) a pointer to the next element 0
  - 0 Search through the chain for virtual page number
- Segment table maps two-dimensional physical addresses
  - Entries protected with valid bits and r/w/x privileges 0



Segmentation example

Page table example

# Virtual Memory

- <u>Virtual memory</u>: separation of user logical memory and physical memory
  - $\circ$  Only part of program needs to be in memory for execution  $\rightarrow$  logical address space > physical address space
  - Allows address spaces to be shared by multiple processes  $\rightarrow$  less swapping
  - Allows pages to be shared during fork(), speeding process creation
- <u>Page fault</u> results from the first time there is a reference to a specific page  $\rightarrow$  traps the OS
  - Must decide to abort if the reference is invalid, or if the desired page is just not in memory yet
    - If the latter: get empty frame, swap page into frame, reset tables to indicate page now in memory, set validation bit, restart instruction that caused the page fault
  - If an instruction accesses multiple pages near each other  $\rightarrow$  less "pain" because of <u>locality of reference</u>
- <u>Demand Paging</u> only brings a page into memory when it is needed  $\rightarrow$  less I/O and memory needed
  - Lazy swapper never swaps a page into memory unless page will be needed
  - Could result in a lot of page-faults
  - Performance: EAT = [(1-p)\*memory access + p\*(page fault overhead + swap page out + swap page in + restart overhead)]; where Page Fault Rate 0 " p " 1
    - if p = 0, no page faults; if p = 1, every reference is a fault
  - Can optimize demand paging by loading entire process image to swap space at process load time
- Pure Demand Paging: process starts with no pages in memory
- <u>Copy-on-Write (COW)</u> allows both parent and child processes to initially share the same pages in memory
- If either process modifies a shared page, only then is the page copied
- <u>Modify (dirty) bit</u> can be used to reduce overhead of page transfers  $\rightarrow$  only modified pages written to disk
- When a page is replaced, write to disk if it has been marked dirty and swap in desired page
- Pages can be replaced using different algorithms: FIFO, LRU (below)
  - Stack can be used to record the most recent page references (LRU is a "stack" algorithm)



page frames

- <u>Second chance algorithm</u> uses a reference bit
  - If 1, decrement and leave in memory
  - If 0, replace next page
- Fixed page allocation: Proportional allocation Allocate according to size of process
  - $s_i = \text{size of process } P_i, S = \Sigma s_i, m = \text{total number of frames}, a_i \text{allocation for } P_i$
  - $a_i = (s_i/S) * m$
- <u>Global replacement</u>: process selects a replacement frame from set of all frames
  - One process can take frame from another
  - Process execution time can vary greatly
  - Greater throughput
  - Local replacement: each process selects from only its own set of allocated frames
    - More consistent performance
    - Possible under-utilization of memory
- Page-fault rate is very high if a process does not have "enough" pages
  - <u>Thrashing</u>: a process is busy swapping pages in and out  $\rightarrow$  minimal work is actually being performed
- <u>Memory-mapped</u> file I/O allows file I/O to be treated as routine memory access by <u>mapping</u> a disk block to a page

in memory <u>I/O Interlock</u>: Pages must sometimes be locked into memory ٠

### Unit 5 – File-System Interface

- <u>File</u> Uniform logical view of information storage (no matter the medium)
  - Mapped onto physical devices (usually nonvolatile)
  - Smallest allotment of nameable storage
  - Types: Data (numeric, character, binary), Program, Free form, Structured
  - Structure decided by OS and/or program/programmer
- Attributes:
  - Name: Only info in human-readable form
  - Identifier: Unique tag, identifies file within the file system
  - Type, Size
  - Location: pointer to file location
  - Time, date, user identification
- File is an <u>abstract data type</u>
- Operations: create, write, read, reposition within file, delete, truncate
- Global table maintained containing process-independent open file information: <u>open-file table</u>
- Per-process open file table contains pertinent info, plus pointer to entry in global open file table
- Open file locking: mediates access to a file (shared or exclusive)
  - <u>Mandatory</u> access denied depending on locks held and requested
  - Advisory process can find status of locks and decide what to do
- File type can indicate internal file structure
- Access Methods: Sequential access, direct access
  - Sequential Access: tape model of a file
  - Direct Access: random access, relative access
- Disk can be subdivided into <u>partitions</u>; disks or partitions can be <u>RAID</u> protected against failure.
  - Can be used raw without a file-system or formatted with a file system
  - Partitions also knows as minidisks, slices
- <u>Volume</u> contains file system: also tracks file system's info in <u>device directory</u> or <u>volume table of contents</u>
- File system can be general or special-purpose. Some <u>special purpose FS</u>:
  - tmpfs temporary file system in volatile memory
  - objfs virtual file system that gives debuggers access to kernel symbols
  - ctfs virtual file system that maintains info to manage which processes start when system boots
  - lofs loop back file system allows one file system to be accessed in place of another
- procfs virtual file system that presents information on all processes as a file system
- Directory is similar to symbol table translating file names into their directory entries
  - Should be efficient, convenient to users, logical grouping
  - <u>Tree structured</u> is most popular allows for grouping
- Commands for manipulating: remove rm<file-name>; make new sub directory mkdir<dir-name>
- <u>Current directory</u>: default location for activities can also specify a <u>path</u> to perform activities in
- <u>Acyclic-graph directories</u> adds ability to directly share directories between users
  - Acyclic can be guaranteed by: only allowing shared files, not shared sub directories; garbage collection; mechanism to check whether new links are OK
- File system must be mounted before it can be accessed kernel data structure keeps track of mount points
- In a file sharing system User IDs and Group IDs help identify a user's permissions
- <u>Client-server</u> allows multiple clients to mount remote file systems from servers <u>NFS</u> (UNIX), <u>CIFS</u> (Windows)
- <u>Consistency semantics</u> specify how multiple users are to access a shared file simultaneously similar to synchronization algorithms from Ch.7
  - One way of protection is Controlled Access: when file created, determine r/w/x access for users/groups





file type	usual extension	function
executable	exe, com, bin or none	ready-to-run machine- language program
object	obj. o	compiled, machine language, not linked
source code	c, cc, java, pas, asm, a	source code in various languages
batch	bat, sh	commands to the comman interpreter
text	txt, doc	textual data, documents
word processor	wp, tex, rtf, doc	various word-processor formats
library	lib, a, so, dll	libraries of routines for programmers
print or view	ps, pdf, jpg	ASCII or binary file in a format for printing or viewing
archive	arc, zip, tar	related files grouped into one file, sometimes com- pressed, for archiving or storage
multimedia	mpeg, mov, rm, mp3, avi	binary file containing audio or A/V information

### **File System Implementation**

- File system resides on secondary storage disks; file system is organized into layers  $\rightarrow$
- <u>File control block</u>: storage structure consisting of information about a file (exist per-file)
- <u>Device driver</u>: controls the physical device; manage I/O devices
- File organization module: understands files, logical addresses, and physical blocks
  - Translates logical block number to physical block number
  - Manages free space, disk allocation
- Logical file system: manages metadata information maintains file control blocks
- <u>Boot control block</u>: contains info needed by system to boot OS from volume
- Volume control block: contains volume details; ex: total # blocks, # free blocks, block size, free block pointers
- <u>Root partition</u>: contains OS; mounted at boot time
- For all partitions, system is consistency checked at mount time
  - Check metadata for correctness only allow mount to occur if so
- Virtual file systems provide object-oriented way of implementing file systems
- Directories can be implemented as Linear Lists or Hash Tables
  - Linear list of file names with pointer to data blocks simple but slow
  - Hash table linear list with hash data structure decreased search time
    - Good if entries are fixed size
    - <u>Collisions</u> can occur in hash tables when two file names hash to same location
- <u>Contiguous allocation</u>: each file occupies set of contiguous blocks







- Simple, best performance in most cases; problem finding space for file, external fragmentation
- <u>Extent</u> based file systems are modified contiguous allocation schemes extent is allocated for file allocation
- <u>Linked Allocation</u>: each file is a linked list of blocks no external fragmentation
  - Locating a block can take many I/Os and disk seeks
- <u>Indexed Allocation</u>: each file has its own <u>index block(s)</u> of pointers to its data blocks
  - Need index table; can be random access; dynamic access without external fragmentation but has overhead
- Best methods: linked good for sequential, not random; contiguous good for sequential and random
- File system maintains <u>free-space list</u> to track available blocks/clusters
- <u>Bit vector</u> or <u>bit map</u> (n blocks): block number calculation → (#bits/word)\*(# 0-value words)+(offset for 1<sup>st</sup> bit)
- 0

Example:

block size = 4KB = 212 bytes disk size = 240 bytes (1 terabyte) n = 240/212 = 228 bits (or 256 MB) if clusters of 4 blocks -> 64MB of memory

- Space maps (used in ZFS) divide device space into <u>metaslab</u> units and manages metaslabs
  - Each metaslab has associated space map
- <u>Buffer cache</u> separate section of main memory for frequently used blocks
- <u>Synchronous</u> writes sometimes requested by apps or needed by OS no buffering
- •
- Asynchronous writes are more common, buffer-able, faster
- Free-behind and read-ahead techniques to optimize sequential access
- <u>Page cache</u> caches pages rather than disk blocks using virtual memory techniques and addresses
  - Memory mapped I/O uses page cache while routine I/O through file system uses buffer (disk) cache
- <u>Unified buffer cache</u>: uses same page cache to cache both memorymapped pages and ordinary file system I/O to avoid <u>double caching</u>

