IAE 2 portions

**CPU Scheduling:** Basic concepts, Scheduling Criteria, Scheduling Algorithms. Thread Scheduling, Multiple-Processor Scheduling, Real-Time CPU Scheduling.

Synchronization: Background, Critical Section Problem, Mutex locks, Semaphores, Classic

Problems of Synchronization.

**Deadlocks:** System Model, Deadlock characterization, Methods for handling deadlocks, Deadlock prevention, Deadlock avoidance, Deadlock Detection and Recovery from deadlock.

PART – A									
Q.No		BT Level	Competence						
1.	List the CPU schedulin	BTL-1	Remembering						
2.	Differentiate short terr	n and long ter	m scheduler.		BTL-4	Analyzing			
3.	Analyse the critical sec	ction problem			BTL-3	Applying			
4.	Show the use of monit	ors in process	synchronizatio	n.	BTL-4	Analyzing			
5.	Mention the use of res	ource-allocati	on graph.		BTL-2	Understanding			
6.	"Priority inversion is a low priority process gained hold of the CPU	BTL-5	Evaluating						
7.	What is meant by 'star	vation' in oper	rating system?		BTL-2	Understanding			
8.	Illustrate operation of	semaphore wi	th example proc	cedure.	BTL-3	Applying			
9.	What is the meaning o	f the term bus	y waiting?		BTL-1	Remembering			
10.	Define deadlock.				BTL-1	Remembering			
11.	Show what are the sch	BTL-3	Applying						
12.	Give the four necessar	BTL-5	Evaluating						
13.	"If there is a cycle in the in deadlock state". Con	BTL-6	Creating						
14.	List out the methods u	BTL-1	Remembering						
15.	Show what are the var	BTL-3	Applying						
16.	Point out the function	BTL-4	Analyzing						
17.	Is it possible to have o	BTL-5	Evaluating						
PART – B									
1.	(i) Define scheduling (ii) Compute the avera preemptive SJF scl Process P1 P2 P3 P4 P5	Explain SJF s ge waiting tin neduling algor Arrival time 0 2 4 5 3	scheduling algon ne for the proce rithm.(5) Burst time 7 4 1 4 4 4 4	ithms . (8) esses using non-	BTL-4	Analyzing			
2.	Describe the difference scheduling with suitab	BTL-1	Remembering						

3.	Discuss how the following pairs of scheduling criteria conflict in certain								in certain		
	settings.								Remembering		
	1. CPU utilization and response time. (4)							(5)	BIT-I	Remembering	
	11. 111	I/O device	unaio utiliz	ation a	nd CPU	utiliza	tion (	4)	ne. ( <i>J</i> )		
4	Consider t	he followin	g set o	f proce	sses wit	h the l	enoth	$\frac{1}{1}$	PIJ-burst		
	time in giv	en ms:	5 500 0	r proce	55 <b>6</b> 5 W10	ii tiic i	engen				
	8										
	Process Burst Time Arrival time										
	P1	8		0							Applying
	P2	4		1						BTL-3	
	P3	9		2							
	P4	5		3							
	P5	3		4							
		0 1 1	.11		.1	, <b>.</b>	6.4				
	Draw four Gantt charts illustrating the execution of these processes usin								esses using		
	FCFS, SJF, priority and RR(quantum=2)scheduling. Also calculat										
5.	Explain th	e difference	es in th	ne degi	ee to w	hich th	ne foll	owing s	scheduling		
5.	algorithms	discrimina	te in fa	vor of	short pro	ocesse	s.	o	seneduning	BTI 1	Analyzing
	(i) RR $(7)$	albertititita			short pr		5.			DIL-4	
	(i) KK (7)	wel feedbac	ek anei	1es (6)							
6	Outline a s	volution to s	olve R	ounded	buffer I	Peader	s and V	Vritors r	mblem and	DTI 5	Englishting
0.	Dining phi	losopher pr	oblem.	(13)	ounci, i	Cauci		v mens p		BIL-3	Evaluating
7	Design ho		nont u	voit() or	daianal	() com	anhor	0.000000	tions $(12)$		
/.	Design no	w to impler	nent w	ait() ar	id signal	() sem	apnor	e opera	tions $(13)$	BTL-6	Creating
8.	Explain Deadlock detection with suitable example. (13)								BTL-4	Analyzing	
9.	Consider t	he snapshot	of a s	ystem(	13)						
	Max Allocation Available										
	ABCD ABCD ABCD										
	P0 2001 4212 3321										
	P1 3121 5252										
	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$										
	P3 1312 1424										
	P4 1432 3665									Understanding	
	Answer the following Using Banker's algorithm,							and an in			
	(1) Illustrate that the system is in safe state by demonstrating an order in which the processes may complete?							order in	D1L-2		
	which the processes may complete? (ii) If a request from process P1 arrives for $(1, 1, 0, 0)$ can the request be							uest be			
	granted immediately?										
	(iii) if the r	equest from	p4 ari	rives fo	r(0,0,2,0	)) can	the rec	juest be	e granted		
	immediate	ly?	-					-	-		
10.	(i) Illu	strate deadl	ock wi	th neat	exampl	e.(7)					
	(ii) The operating system contains 3 resources, the number								nber		
	of instance of each resource type are 7,7,10. The current										
	resource allocation state is as shown below.										
	Current Allocation Max Need										
			R1	R2	R3	R1	R2	R3			
		P1	2	2	3	3	6	8		BTL-1	Remembering
		P2	2	0	3	4	3	3			
		P3	1	2	4	3	4	4			
L		-				_					

	Is the current allocation in a safe state? (6)		
11.	Discuss in detail the critical section problem and write the algorithm for producer consumer problem.(13)	BTL-2	Understanding
12.	(i) Is it possible to have concurrency but not parallelism? Explain.(6)		
	(ii) Consider a system consisting of four resources of the same type that		
	are shared by three processes, each of which needs at most two resources.		
	Show that the system is deadlock free. (7)	BTL-3	Applying
13	Describe what is deadlock. Write about deadlock condition and banker's		
	algorithm in detail (13)	BTL-2	Understandi ng
14	For below Processes table, calculate the average waiting time for the		
	algorithms:		
	• First Come First Serve (FCFS) (4)		
	• Shortest Job First (SJF) and (4)		
	• Priority Scheduling (5)		Applying
	Process Burst Time Priority	BTL-3	11 5 8
	P1 10 3	DILS	
	$P_2$ 1 1		
	P3 2 4		
	P4 1 5 D5 5 2		
15	Fyluate and explain the conditions for deadlock prevention (13)		
15	Evaluate and explain the conditions for deadlock prevention. (13)	BTL-5	Evaluating
	PART - C		
1.	Which of the following scheduling algorithms could result in starvation?		
	(i)First-come, first-served (5)		
	(ii) Shortest job first (5)		
	(iii) Round robin (5)	BTL-6	Creating
	Detail with Justification.		8
2.	(i).Consider the following set of processes with the length of CPU burst time given in milliseconds.		
	Process Burst Time priority Arrival		
	Time		
	P1 10 3 0		
	P2 1 1 1		
	P3 2 3 2	BTL-5	
	P4 1 4 1		Evaluating
	P5 5 2 2		
	Draw the Gantt chart for the execution of these processes using FCFS,		
	SJF, SRTS, pre-emptive and non pre-emptive priority and Round robin		
	with the time slice of 2ms, Find average waiting time and turnaround time		
	(ii) Explain multi level queue and multi level feedback queue scheduling		
	with suitable examples (5)		
	(5)		
3.	Consider a system consisting of 'm' resources of the same type, being		Evaluating
	shared by 'n' processes. Resources can be requested and released by		-
	processes only one at a time. Show that the system is deadlock free if the	BTL-5	
	following two conditions hold: (15)		
	a) The maximum need of each process is between 1 and m resources		
	b) The sum of all maximum needs is less than m+n.		

4.	Consider the following system snapshot using data structures in the		
	Banker's algorithm with resources A,B,C and D and process P0 to P4:		
	Max Allocation Available Need		
	ABCD ABCD ABCD ABCD		Evaluating
	P0 6012 4001 3211		
	P1 1750 1100		
	P2 2356 1254	BTL-5	
	P3 1653 0633		
	P4 1656 0212		
	Using Banker's algorithm, answer the following questions: (i)How		
	many resources of type A,B,C and D are there? (3) (ii)What are the		
	contents of the need matrix? (3)		
	(iii) Is the system in a safe state? Why? (3)		
	(iv) If a request from process P4 arrives for additional resources of		
	(1,2,0,0)		
	can the banker's algorithm grant the request immediately? Show the		
	new system state and other criteria. (6)		
5.	Consider the following set of processes with the length of the CPU-burst		
	time in given ms: all 5 processess arrive at time 0 in the order given.		
	Process Burst Time		
	P1 10		
	P2 29		
	P3 03	BTL-6	Creating
	P4 07		
	P5 12		
	Draw four Gantt charts illustrating the execution of these processes		
	using FCFS, SJF, priority and RR(quantum=10)scheduling.		
	Also calculate average waiting time and turnaround time for each		
	scheduling algorithms. (15)		

1.Define CPU scheduling.

CPU scheduling is the process of switching the CPU among various processes. CPU scheduling is the basis of multiprogrammed operating systems. By switching the CPU among processes, the operating system can make the computer more productive.

2. What is preemptive and nonpreemptive scheduling?

Under nonpreemptive scheduling once the CPU has been allocated to a process, the process keeps the CPU until it releases the CPU either by terminating or switching to the waiting state. Preemptive scheduling can preempt a process which is utilizing the CPU in between its execution and give the CPU to another process.

# 3. What is a Dispatcher?

The dispatcher is the module that gives control of the CPU to the process selected by the short-term scheduler. This function involves:

- Switching context
- Switching to user mode
- Jumping to the proper location in the user program to restart that program.

## 4. What is dispatch latency?

The time taken by the dispatcher to stop one process and start another running is known as ispatch latency.

5. What are the various scheduling criteria for CPU scheduling?

The various scheduling criteria are

• CPU utilization

- Throughput
- Turnaround time
- Waiting time
- Response time

# 6.Define throughput?

Throughput in CPU scheduling is the number of processes that are completed per unit time. For long processes, this rate may be one process per hour; for short transactions, throughput might be 10 processes per second.

# 7.What is turnaround time?

Turnaround time is the interval from the time of submission to the time of completion of a process. It is the sum of the periods spent waiting to get into memory, waiting in the ready queue, executing on the CPU, and doing I/O.

## 8.Define race condition.

When several process access and manipulate same data concurrently, then the outcome of the execution depends on particular order in which the access takes place is called race condition. To avoid race condition, only one process at a time can manipulate the shared variable.

# 9.What is critical section problem?

Consider a system consists of 'n' processes. Each process has segment of code called a critical section, in which the process may be changing common variables, updating a table, writing a file. When one process is executing in its critical section, no other process can allowed to execute in its critical section.

10. What are the requirements that a solution to the critical section problem must satisfy?

The three requirements are

- Mutual exclusion
- Progress
- Bounded waiting

11.Define entry section and exit section.

The critical section problem is to design a protocol that the processes can use to cooperate. Each process must request permission to enter its critical section. The section of the code implementing this request is the entry section. The critical section is followed by an exit section. The remaining code is the remainder section.

12.Give two hardware instructions and their definitions which can be used for implementing mutual exclusion.TestAndSet

```
boolean TestAndSet (boolean &target)
{
    boolean rv = target;
    target = true;
    return rv;
    }
    • <u>Swap</u>
    void Swap (boolean &a, boolean &b)
    {
        boolean temp = a;
        a = b;
        b = temp;
    }
```

#### 13. What is semaphores?

A semaphore 'S' is a synchronization tool which is an integer value that, apart from initialization, is accessed only through two standard atomic operations; wait and signal. Semaphores can be used to deal with the n-process critical section problem. It can be also used to solve various Synchronization problems.

## 14. Define busy waiting and spinlock.

When a process is in its critical section, any other process that tries to enter its critical section must loop continuously in the entry code. This is called as busy waiting and this type of semaphore is also called a spinlock, because the process while waiting for the lock.

15. What resources are used when a thread is created? How do they differ from those used when a process is created?

Because a thread is smaller than a process, thread creation typically uses fewer resources than process creation. Creating a process requires allocating a process control block (PCB), a rather large data structure. The PCB includes a memory map, list of open files, and environment variables. Allocating and managing the memory map is typically the most time-consuming activity. Creating either a user or kernel thread involves allocating a small data structure to hold a register set, stack, and priority.

16. What advantage is there in having different time-quantum sizes on different levels of a multilevel queueing system?

Processes that need more frequent servicing, for instance, interactive processes such as editors, can be in a queue with a small time quantum. Processes with no need for frequent servicing can be in a queue with a larger quantum, requiring fewer context switches to complete the processing, making more efficient use of the computer

## 17. Define deadlock.

A process requests resources; if the resources are not available at that time, the process enters a wait state. Waiting processes may never again change state, because the resources they have requested are held by other waiting processes. This situation is called a deadlock.

## 18. What is the sequence in which resources may be utilized?

Under normal mode of operation, a process may utilize a resource in the following sequence:

• Request: If the request cannot be granted immediately, then the requesting process must wait until it can acquire the resource.

- Use: The process can operate on the resource.
- Release: The process releases the resource.

## 19. What are conditions under which a deadlock situation may arise?

A deadlock situation can arise if the following four conditions hold simultaneously in a system:

- a. Mutual exclusion
- b. Hold and wait
- c. No pre-emption

## 20. What is a resource-allocation graph?

Deadlocks can be described more precisely in terms of a directed graph called a system resource allocation graph. This graph consists of a set of vertices V and a set of edges E. The set of vertices V is partitioned into two different types of nodes; P the set consisting of all active processes in the system and R the set consisting of all resource types in the system.

## 21. Define request edge and assignment edge.

A directed edge from process Pi to resource type Rj is denoted by  $Pi \rightarrow Rj$ ; it signifies that process Pi requested an instance of resource type Rj and is currently waiting for that resource. A directed edge from

resource type Rj to process Pi is denoted by Rj $\rightarrow$ Pi, it signifies that an instance of resource type has been allocated to a process Pi. A directed edge Pi $\rightarrow$ Rj is called a request edge. A directed edge Rj $\rightarrow$ Pi is called an assignment edge.

22. What are the methods for handling deadlocks?

The deadlock problem can be dealt with in one of the three ways:

a. Use a protocol to prevent or avoid deadlocks, ensuring that the system will never enter a deadlock state.

- b. Allow the system to enter the deadlock state, detect it and then recover.
- c. Ignore the problem all together, and pretend that deadlocks never occur in the system.

#### 23. Define deadlock prevention.

Deadlock prevention is a set of methods for ensuring that at least one of the four necessary conditions like mutual exclusion, hold and wait, no preemption and circular wait cannot hold. By ensuring that that at least one of these conditions cannot hold, the occurrence of a deadlock can be prevented.

## 24. Define deadlock avoidance.

An alternative method for avoiding deadlocks is to require additional information about how resources are to be requested. Each request requires the system consider the resources currently available, the resources currently allocated to each process, and the future requests and releases of each process, to decide whether the could be satisfied or must wait to avoid a possible future deadlock.

#### 25. What are a safe state and an unsafe state?

A state is safe if the system can allocate resources to each process in some order and still avoid a deadlock. A system is in safe state only if there exists a safe sequence. A sequence of processes <P1,P2,....Pn> is a safe sequence for the current allocation state if, for each Pi, the resource that Pi can still request can be satisfied by the current available resource plus the resource held by all the Pj, with j<i. if no such sequence exists, then the system state is said to be unsafe.

## 26. What is banker's algorithm?

Banker's algorithm is a deadlock avoidance algorithm that is applicable to a resource-allocation system with multiple instances of each resource type. The two algorithms used for its implementation are:

a. Safety algorithm: The algorithm for finding out whether or not a system is in a safe state.

b. Resource-request algorithm: if the resulting resource allocation is safe, the transaction is completed and process Pi is allocated its resources. If the new state is unsafe Pi must wait and the old resource-allocation state is restored.