

M/S 59
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Register No.

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SNS COLLEGE OF ENGINEERING

Kurumbapalayam (Po), Coimbatore - 641 107

AN AUTONOMOUS INSTITUTION

Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai



INTERNAL ASSESSMENT EXAMINATION - I

IV Semester

B.E., CSE-IOT & CYBERSECURITY INCLUDING BLOCKCHAIN TECHNOLOGY

19SB405 - Microprocessors and Advanced Microcontrollers

Regulations 2019

Duration : 1 Hours 30 Minutes

Date : 23.03.2023

Session : FN

Maximum: 50 Marks

Answer ALL questions

PART A - (5 X 2 = 10 marks)

Q.No	Question	M	CO	BL
1.	How multi processors can be connected in 8086?	2	CO-1	L-2
2.	Write the instruction used to clear the carry Flag.	2	CO-1	L-2
3.	Define Assembler Directives.	2	CO-1	L-1
4.	List the advantages of DSP.	2	CO-2	L-1
5.	Compare Floating and Fixed point processors.	2	CO-2	L-1

PART B - (2 X 13 = 26 marks)

6.	(a) Describe the Architecture of 8085 with neat diagram.	13	CO-1	L-2
	OR			
	(b) Classify the instruction set of 8086 with examples.	13	CO-1	L-2
7.	(a) (i) Explain the addressing modes of 8086 with an example.	7	CO-1	L-2
	(ii) Discuss string manipulation instructions.	6	CO-1	L-2
	OR			
	(b) Analyze and conclude how the DSP is faster in manipulation of instructions than Microprocessor.	13	CO-2	L-4

PART C - (1 x 14 = 14 Marks)

8.	(a) Create a program to perform Division, Multiplication and sorting of Numbers in Ascending and Descending order.	14	CO-1	L-4
	OR			
	(b) Analyze the architecture and working of DSP with neat sketch.	14	CO-2	L-2

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