Accredited by NBA - AICTE and Accredited by NAAC - UGC with 'A++' Grade Approved by AICTE, New Delhi \& Affiliated to Anna University, Chennai

## DEPARTMENT OF ELECTRONICS \& COMMUNICATION ENGINEERING

# 19ECB202 - LINEAR AND DIGITAL CIRCUITS 

II YEAR/ III SEMESTER

UNIT 4 - COMBINATIONAL and SEQUENTIAL CIRCUITS
TOPIC - Code Converters (Excess 3 to BCD and BCD to Excess 3)

## What is a Excess- 3 and BCD code?

Excess-3 code is non-weighted and self complementary code.

BCD is a class of binary encodings of decimal numbers where each digit is represented by a fixed number of bits, usually four or eight.

## BCD to Excess 3

- 



## BCD to Excess 3

| MOP NAT |  |  |  | EMES-3 ¢0-4T |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Hi | E | 61 | E) | 13 | 12 | 11 | 10 |
| 0 | 0 | 0 | 0 | 0 | 0 | 1. | 1 |
| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 |
| 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 |
| 0 | 0 | 1 | 1 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 |
| 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 |
| 0 | 1 | 1 | 6 | 1 | 0 | 6 | 1 |
| 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 |
| 1 | 0 | 4 | 0 | 1 | 0 | 1 | 1 |
| 1 | 0 | 0 | 1 | 1 | 1 | 6 | 0 |
| 1 | 0 | 1 | 6 | X | X | 5 | X |
| 1 | 0 | 1 | 1 | x | $x$ | T | x |
| 1 | 1 | 0 | 6 | X | X | I | \% |
| 1 | 1 | 0 | 1 | x | X | 5 | X |
| 1 | 1 | 1 | 0 | x | $x$ | T | x |
| 1 | 1 | 1 | 1 | x | x | 早 | x |

## BCD to Excess 3






## ACTIVITY

$3+3$
Puzzle time
121.015

|  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | 12 |  |  |  | 13 |  |
|  |  |  |  |  |  |  |


|  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | 14 |  |  |  | 15 |  |
|  |  |  |  |  |  |  |

## Cian you put the mumbers 1

 tho is im each off the squumares so that each side adds up to the nimidile mumber?
## Excess 3 to BCD






## Excess 3 to BCD

Truth Table:


MAP=
wore


## Excess- 3 to BCD Code



## ASSESSMENT

1. $\qquad$ a $\qquad$ is used in simplifying $\qquad$ b $\qquad$
b) APM ARNAKGHU
a) RACE TOND NOCIDIONT
2. Name the gate which is called a coincidence detector? Justify
3. Name the gate which can be used as switch? Justify your reason
4)How many AND gates and OR gates are required to realize the expression $Y=B D+C A+E F+G H$. Explain
5)The complement function can be done by using an $\qquad$ (RTENIERV

THANK YOU

