



# **SNS COLLEGE OF TECHNOLOGY**

**Coimbatore-35**  
**An Autonomous Institution**



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Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai

## **DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

### **19ECB231- DIGITAL ELECTRONICS**

**II YEAR/ III SEMESTER**

**UNIT 1 – MINIMIZATION TECHNIQUES & LOGIC GATES**

**TOPIC – BOOLEAN POSTULATES AND LAWS**



# BOOLEAN ALGEBRA



- In 1854 George Boole introduced a systematic treatment of logic and developed an algebraic system known as symbolic logic, or **Boolean algebra**.
- Boolean algebra is a branch of mathematics used to describe the manipulation and processing of **binary** information.
- The two-valued Boolean algebra has important application in the design of modern computing systems.
- Boolean algebra is algebra for the manipulation of objects that can take on only **two** values, typically true and false.
- It is common to interpret the digital value **0** as false and the digital value **1** as true.



# BOOLEAN ALGEBRA



- Boolean algebra is a form of algebra that deals with single digit binary values and variables.
- Values and variables can indicate some of the following binary pairs of values:
  - ON / OFF
  - TRUE / FALSE
  - HIGH / LOW
  - CLOSED / OPEN
  - 1 / 0
- Three fundamental operators in Boolean algebra
- **NOT**: unary operator that complements represented as **A'**.
- **AND**: binary operator which performs logical multiplication i.e. AND ed with would be represented as **A.B**
- **OR**: binary operator which performs logical addition i.e. OR ed with would be represented as **A+B**



## BOOLEAN ALGEBRA PRECEDENCE OF OPERATORS



Boolean expressions must be evaluated with the following order of operator precedence

parentheses

NOT

AND

OR

Example:

$$F = \overline{A(C + \overline{BD}) + \overline{BC}} \overline{E}$$



$$F = \left( \underbrace{A \left( \underbrace{C + \underbrace{\overline{BD}}_{\text{AND}}} \right)}_{\text{OR}} + \underbrace{\overline{BC}}_{\text{AND}} \right) \underbrace{\overline{E}}_{\text{NOT}}$$



**Logic Gates** - Boolean functions are implemented in digital computer circuits called **gates**.

- A gate is an electronic device that produces **a result** based on two or more input values.
  - Gates consist of one to six **transistors**, but digital designers think of them as a single unit.
  - Integrated circuits contain collections of gates suited to a particular purpose.

### Symbols for Logic Gates

The three simplest gates are the AND, OR, and NOT gates.

The image shows three logic gates: AND, OR, and NOT. Each gate is represented by a symbol, a label, and a truth table.

**AND Gate:** The symbol is a D-shaped gate with two inputs labeled X and Y, and one output labeled XY. Below it is the label "X AND Y" and a truth table.

X	Y	XY
0	0	0
0	1	0
1	0	0
1	1	1

**OR Gate:** The symbol is a gate with a curved input side and a pointed output side, with two inputs labeled X and Y, and one output labeled X+Y. Below it is the label "X OR Y" and a truth table.

X	Y	X+Y
0	0	0
0	1	1
1	0	1
1	1	1

**NOT Gate:** The symbol is a triangle with a small circle at its tip, with one input labeled X and one output labeled  $\bar{X}$ . Below it is the label "NOT X" and a truth table.

X	$\bar{X}$
0	1
1	0



Another very useful gate is the exclusive OR (XOR) gate.

- The output of the XOR operation is true only when the values of the inputs differ.

**X XOR Y**

X	Y	$X \oplus Y$
0	0	0
0	1	1
1	0	1
1	1	0

The diagram shows a standard XOR gate symbol with two inputs labeled X and Y, and one output labeled  $X \oplus Y$ .

### Universal Gates

- Two other common gates are NAND and NOR, which produce complementary output to AND and OR.

**X NAND Y**

X	Y	$X \text{ NAND } Y$
0	0	1
0	1	1
1	0	1
1	1	0

The diagram shows a NAND gate symbol with two inputs labeled X and Y, and one output labeled  $\overline{XY}$ .

**X NOR Y**

X	Y	$X \text{ NOR } Y$
0	0	1
0	1	0
1	0	0
1	1	0

The diagram shows a NOR gate symbol with two inputs labeled X and Y, and one output labeled  $\overline{X+Y}$ .

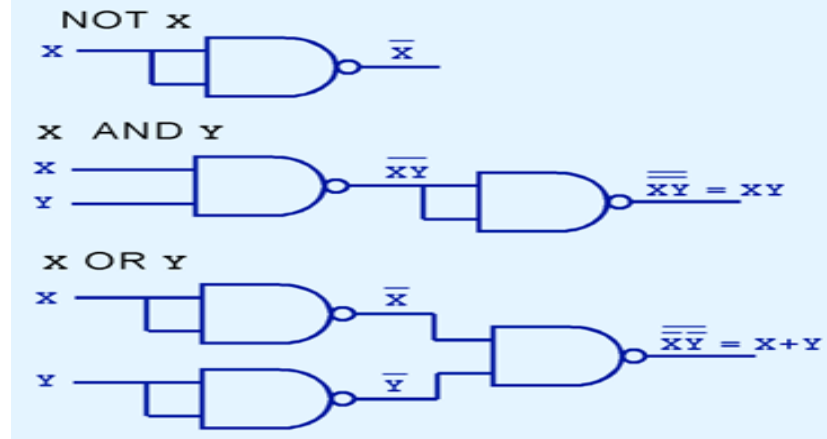
**Logic Equations:**

- $\overline{X+Y} = \overline{X} \overline{Y}$  (NOR gate)
- $\overline{\overline{X} \overline{Y}} = X+Y$  (NAND gate)

The truth table and Logic Symbols for NAND and NOR Gates



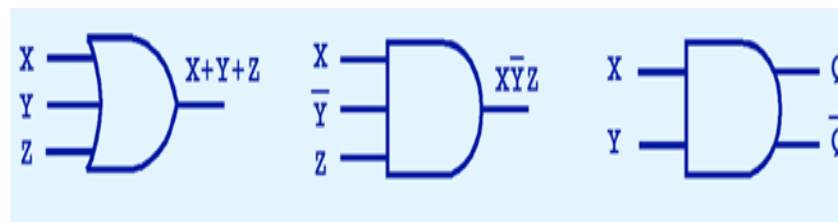
- NAND and NOR are known as universal gates because they are inexpensive to manufacture and any Boolean function can be constructed using only NAND or only NOR gates.



### Three Circuits Constructed Using Only NAND Gates

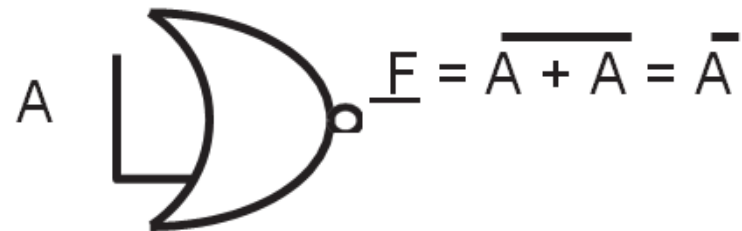
#### Multiple Input Gates

- Gates can have multiple inputs and more than one output.





## 1. “NOT” GATE (INVERTER)



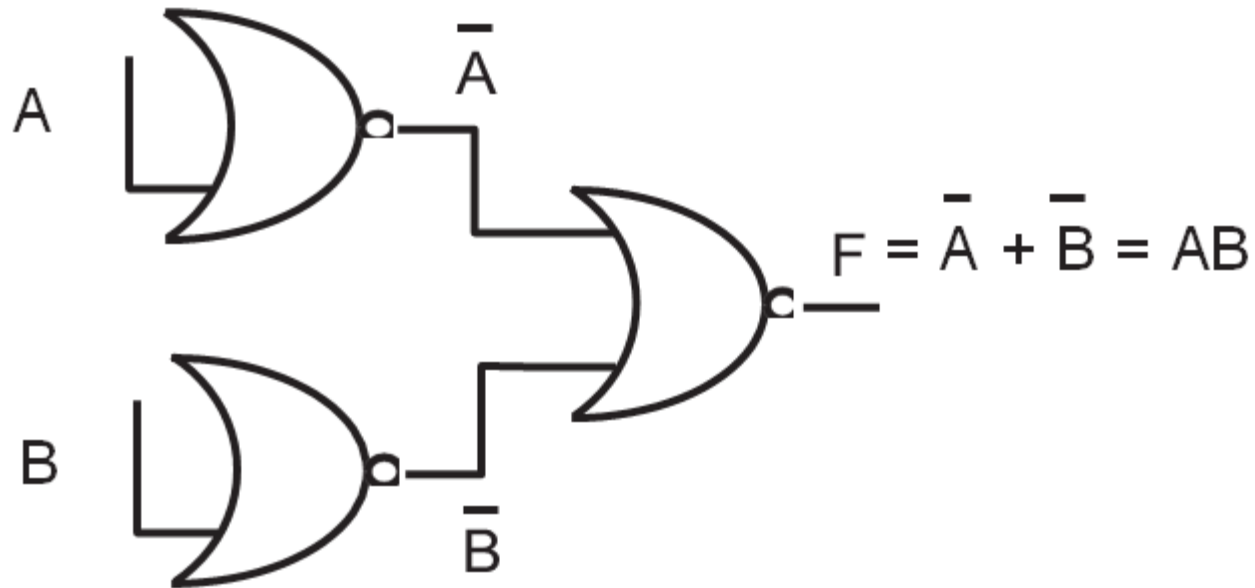
## 2. “OR” GATE







### 3. “AND” GATE





# BOOLEAN ALGEBRA BASIC IDENTITIES



$$\mathbf{X + 0 = X}$$

$$\mathbf{X + 1 = 1}$$

$$\mathbf{X + X = X}$$

$$\mathbf{X + X' = 1}$$

$$\mathbf{(X')' = X}$$

$$\mathbf{X + Y = Y + X}$$

$$\mathbf{X + (Y + Z) = (X + Y) + Z}$$

$$\mathbf{X(Y + Z) = XY + XZ}$$

$$\mathbf{X + XY = X}$$

$$\mathbf{X + X'Y = X + Y}$$

$$\mathbf{(X + Y)' = X'Y'}$$

$$\begin{aligned} \mathbf{XY + X'Z + YZ} \\ \mathbf{= XY + X'Z} \end{aligned}$$

$$\mathbf{X \cdot 1 = X}$$

$$\mathbf{X \cdot 0 = 0}$$

$$\mathbf{X \cdot X = X}$$

$$\mathbf{X \cdot X' = 0}$$

$$\mathbf{XY = YX}$$

$$\mathbf{X(YZ) = (XY)Z}$$

$$\mathbf{X + YZ = (X + Y)(X + Z)}$$

$$\mathbf{X(X + Y) = X}$$

$$\mathbf{X(X' + Y) = XY}$$

$$\mathbf{(XY)' = X' + Y'}$$

$$\begin{aligned} \mathbf{(X + Y)(X' + Z)(Y + Z)} \\ \mathbf{= (X + Y)(X' + Z)} \end{aligned}$$

Identity

Idempotent Law

Complement

Involution Law

Commutativity

Associativity

Distributivity

Absorption Law

Simplification

DeMorgan's Law

Consensus Theorem



$A+AB$	$= A(1+B)$	
	$= A . 1$	(Rule 2)
	$= A$	(Rule 4)

$A + AB =$	
$= (A + AB) + AB$	(Rule 10)
$= (AA + AB) + AB$	(Rule 7)
$= AA + AB + AA \underline{+} AB$ i.e. adding $AA = 0$	(Rule 8)
$= (A+A)(A + B)$	(Factoring)
$= 1 . (A + B)$	(Rule 6)
$= A + B$	



$(A + B)(A + C) =$ $= AA + AC + AB + BC$	(Distrib.)
$= A + AC + AB + BC$	(Rule 7)
$= A(1+C) + AB + BC$	(Distrib.)
$= A . 1 + AB + BC$	(Rule 4)
$= A + AB + BC$	(Rule 2)
$= A(1 + B) + BC$	(Distrib.)
$= A . 1 + BC$	(Rule 2)
$= A + BC$	(Rule 4)



# BOOLEAN ALGEBRA DUALITY PRINCIPLE



- **Principle of Duality :**

- States that a Boolean equation remains valid if we take the dual of the expressions on both sides of the equals sign.
- The dual can be found by interchanging the AND and OR operators along with also interchanging the 0's and 1's.
- This is evident with the duals in the basic identities.
- For instance: DeMorgan's Law can be expressed in two forms

$$(X + Y)' = X'Y' \quad \text{as well as} \quad (XY)' = X' + Y'$$



$$\overline{(xy)} = \bar{x} + \bar{y} \quad \text{and} \quad \overline{(x+y)} = \bar{x}\bar{y}$$



x	y	(xy)	$\overline{(xy)}$	$\bar{x}$	$\bar{y}$	$\bar{x} + \bar{y}$
0	0	0	1	1	1	1
0	1	0	1	1	0	1
1	0	0	1	0	1	1
1	1	1	0	0	0	0

1. THIS STATES THAT THE INVERSE (i.e.) OF A PRODUCT [AND] IS EQUAL TO THE SUM [OR] OF THE COMPLEMENTS

2. THIS STATES THAT THE INVERSE (COMPLEMENT) OF A SUM [OR] IS EQUAL TO THE PRODUCT [AND] OF THE COMPLEMENTS

### TABLE 3.6 Truth Tables for the AND Form of De Morgan's Law COMPLEMENTS:

$F(x, y, z) = x' + yz'$  and its complement,  $F'(x, y, z) = x(y' + z)$

x	y	z	$yz'$	$\bar{x} + yz'$	$\bar{y} + z$	$x(\bar{y} + z)$
0	0	0	0	1	1	0
0	0	1	0	1	1	0
0	1	0	1	1	0	0
0	1	1	0	1	1	0
1	0	0	0	0	1	1
1	0	1	0	0	1	1
1	1	0	1	1	0	0
1	1	1	0	0	1	1

TABLE 3.7 Truth Table Representation for a Function and Its Complement



# De Morgan's Theorem 1

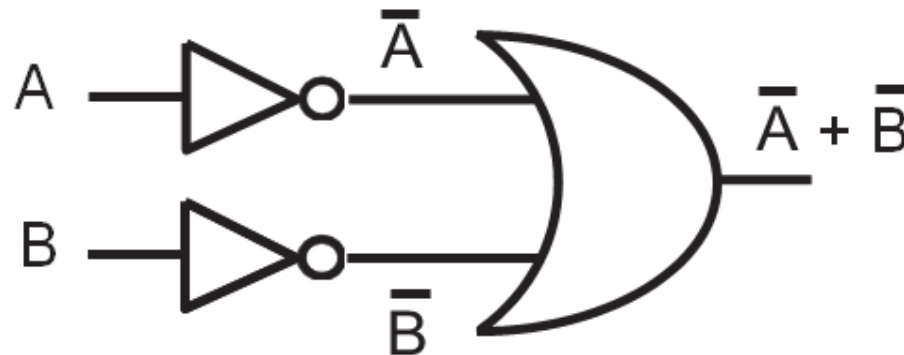
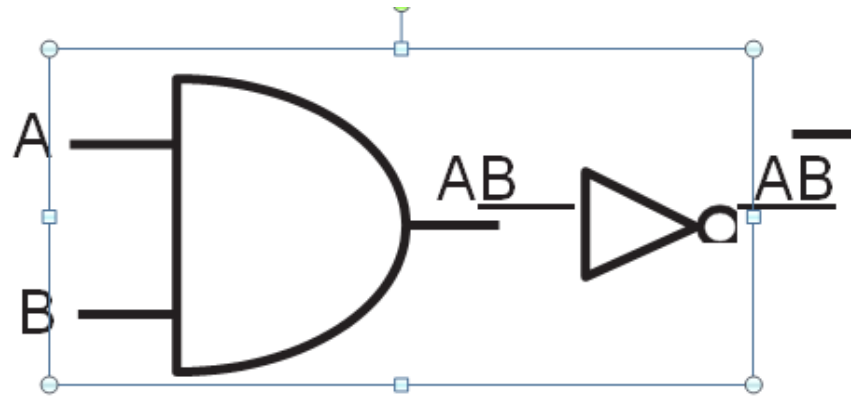
**Theorem 1**  $\overline{A \cdot B} = \overline{A} + \overline{B}$

**Theorem 1**  $\overline{A + B} = \overline{A} \cdot \overline{B}$

A	B	$\overline{A \cdot B}$	$\overline{A}$	$\overline{B}$	$\overline{A} + \overline{B}$
0	0	1	1	1	1
0	1	1	1	0	1
1	0	1	0	1	1
1	1	0	0	0	0



# PROOF OF (1): $(AB)' = A' + B'$







## De Morgan's Theorem 2

**Theorem 2  $A + B = \overline{A \cdot B}$**

$$\overline{A + B} = \overline{A} \cdot \overline{B}$$

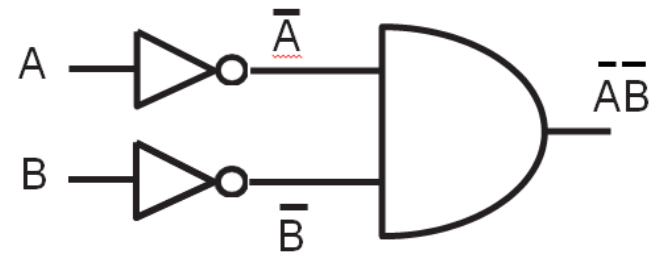
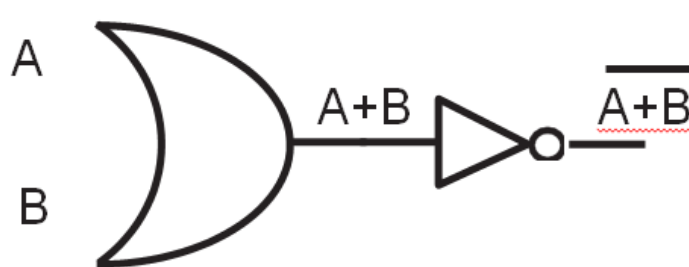
NOR = Bubbled AND



# PROOF OF (2): $\overline{A+B} = \bar{A} \cdot \bar{B}$



A	B	$\overline{A+B}$	A	B	A+B	$\bar{A} \cdot \bar{B}$
0	0	0	1	1	1	1
0	1	1	1	0	0	0
1	0	1	0	1	0	0
1	1	1	0	0	0	0





# EXAMPLES :



- Simplify the following expression :

$$F = BC + B\bar{C} + BA$$

$$F = A + \bar{A}B + \bar{A}\bar{B}C + \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}\bar{C}\bar{D}E$$

$$F = B(C + \bar{C}) + BA$$

Simplification

$$F = B \cdot 1 + BA$$

$$F = A + \bar{A}(B + \bar{B}C + \bar{B}\bar{C}D + \bar{B}\bar{C}\bar{D}E)$$

$$F = B(1 + A)$$

$$F = A + B + \bar{B}C + \bar{B}\bar{C}D + \bar{B}\bar{C}\bar{D}E$$

$$F = B$$

$$F = A + B + \bar{B}(C + \bar{C}D + \bar{C}\bar{D}E)$$

$$F = A + B + C + \bar{C}D + \bar{C}\bar{D}E$$

$$F = A + B + C + \bar{C}(D + \bar{D}E)$$

$$F = A + B + C + D + \bar{D}E$$

$$F = A + B + C + D + E$$



Simplification using Boolean Algebra laws :

$$Z = A'BC + AB'C' + AB'C + ABC' + ABC$$

$$= A'BC + AB'(C' + C) + AB(C' + C)$$

$$= A'BC + AB' + AB$$

$$= A'BC + A(B' + B)$$

$$= A'BC + A$$

$$= BC + A$$

$$(X \cdot Y') + Y = X + Y \text{ with } X=BC \text{ and } Y=A$$



- We can use Boolean identities to simplify the function as follows:

$$F(X, Y, Z) = (X + Y) (X + \bar{Y}) (\overline{XZ})$$

$(X + Y) (X + \bar{Y}) (\overline{XZ})$	Idempotent Law (Rewriting)
$(X + Y) (X + \bar{Y}) (\bar{X} + Z)$	DeMorgan's Law
$(XX + X\bar{Y} + XY + Y\bar{Y}) (\bar{X} + Z)$	Distributive Law
$((X + Y\bar{Y}) + X(Y + \bar{Y})) (\bar{X} + Z)$	Commutative & Distributive Laws
$((X + 0) + X(1)) (\bar{X} + Z)$	Inverse Law
$X(\bar{X} + Z)$	Idempotent Law
$X\bar{X} + XZ$	Distributive Law
$0 + XZ$	Inverse Law
$XZ$	Idempotent Law



## Practice Problems

1.  $AB+(AC)'+AB'C(AB+C)=1$
2.  $ABC+ABC'+AB'C=A(B+C)$
3.  $Y=(A'C[A'BD]'+A'BC'D'+AB'C$
4.  $(A+B)(A'C'+C)(B'+AC)'=A'B$
5. If  $AB'+A'B=C$  , show that  $AC'+A'C=B$
6.  $AB+BC+B'C=AB+C$
7.  $A'B+AB+A'B'$



**THANK YOU**