

SNS COLLEGE OF TECHNOLOGY



Coimbatore-35
An Autonomous Institution

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

19ECB231 – DIGITAL ELECTRONICS

BCD ADDER,BINARY MULTIPLIER /19ECB231/ Digital Electronics /P.UmaMAheswari/AP/ECE/SNSCT

II YEAR/ III SEMESTER

UNIT 2 – COMBINATIONAL CIRCUITS

TOPIC - BCD ADDER, BINARY MULTIPLIER





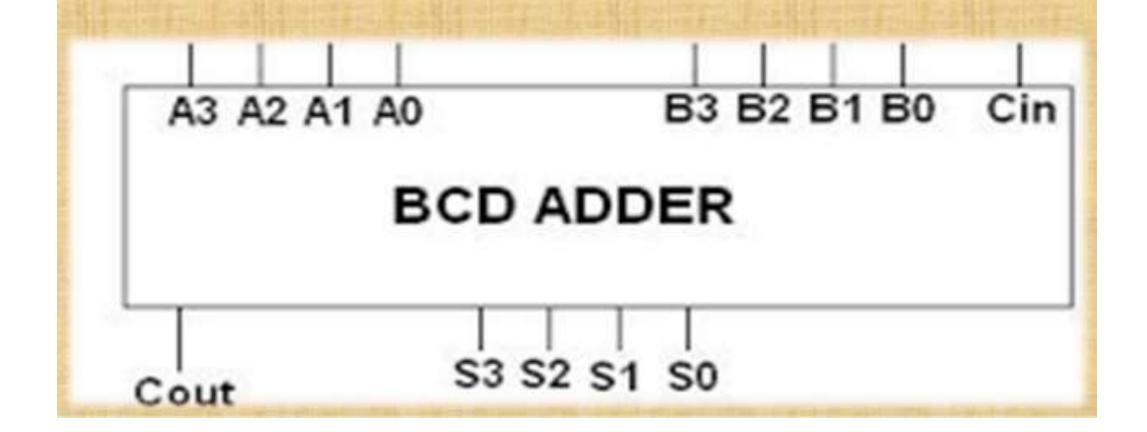
A 4-bit binary adder that is capable of adding two 4-bit words having a BCD (binary-coded decimal) format. The result of the addition is a BCD-format 4-bit output word, representing the decimal sum of the addend and augend, and a carry that is generated if this sum exceeds a decimal value of 9.





FUNCTIONS OF BCD ADDER

- A 4-bit BCD code's used to represent 0 to 9 digits.
- Adding BCD numbers using BCD addition.
- Adding 6 with the sum while exceeding 9 and generating a carry.
- By adding 6 to the sum, make an invalid digit valid.







D	Decimal		BCD				
	7	0	1	1	1		
+	1	0	0	0	1		
	8	1	0	0	0	←	Su
Here, the sum is correct a	and is in the tr	rue BCD	forn	n.			
(ii) Case II: Sum greate:	r than 9 but ca	arry = 0					
I	Decimal			BCD			
	7	0	1	1	1		
+	4	0	1	0	0		
	11	1	0	1	1		
		Inv	alid B	ČD nu	mber		





Here, it may noted that the sum 1011 is invalid BCD number, so, the answer is not correct. Hence, to correct the answer, we add six (0110) to the invalid BCD answer as under:

(iii) Case III: Sum less than or equal to 9 but carry = 1 Let us consider the following addition:

The result of addition is $0001\ 0001 = (11)_{10}$ which is not correct. Hence, to correct the wrong result, we have to add six (0110) as shown below:



WHY BCD ADDER IS USED?



The BCD-Adder is used in the computers and the calculators that perform arithmetic operation directly in the decimal number system. The BCD-Adder accepts the binary-coded form of decimal numbers. The Decimal-Adder requires a minimum of nine inputs and five outputs.



WHY BCD IS CALLED 8421 CODE?



The BCD_{8421} code is so called because each of the four bits is given a 'weighting' according to its column value in the binary system.



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TRUTH TABLE

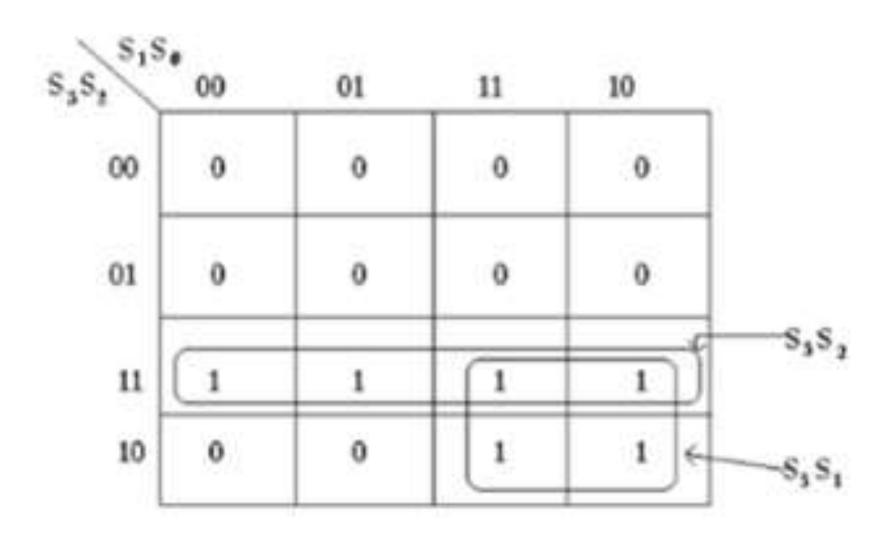


Inp	uts			Output
S_3	S_2	51	So	Y
0	0	0	О	0
0	0	0	1	0
0	0	1	0	0
o	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1.	0
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1





K-map:

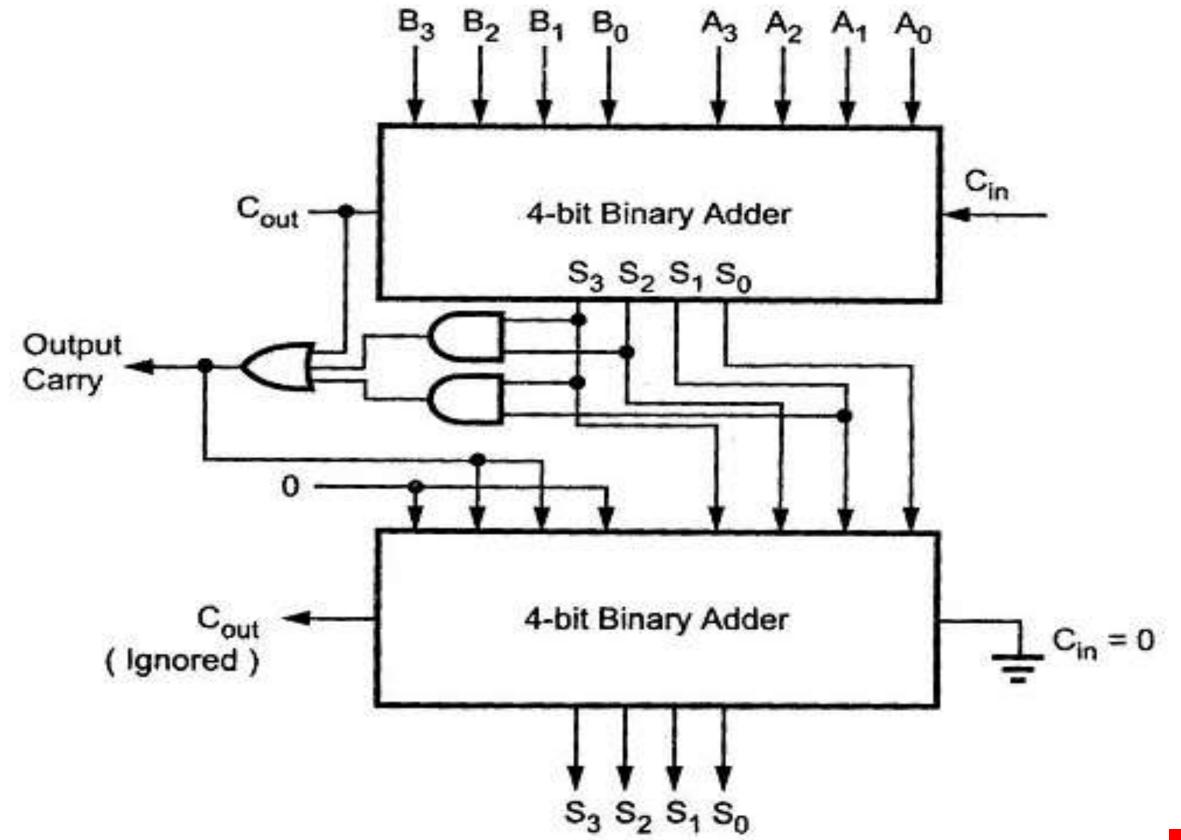


The Boolean expression is

$$Y = S_3 S_2 + S_3 S_1$$











Case II: Sum > 9 and carry = 0

If S₃ S₂ S₁ S₀ of adder-1 is greater than 9, then output Y of combinational circuit becomes 1.

Therefore, $B_3 B_2 B_1 B_0 = 0110$ (of adder-2)

Hence, six (0110) will be added to the sum output of adder-1. We get the corrected BCD result at the output of adder-2.

Case III: Sum ≤ 9 but carry = 1

As carry output of adder-1 is high, we have, Y'=1.

Therefore, $B_3 B_2 B_1 B_0 = 0.110$ (of adder-2)

Hence, 0 1 1 0 will be added to the sum output of adder-1. We get the corrected BCD result at the output of adder-2. The carried out using the binary adder.



WHAT IS BINARY MULTIPLIER?

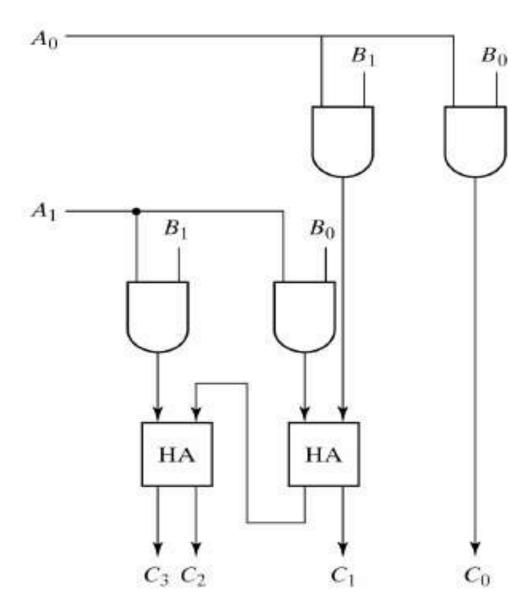


Multiply two binary numbers. It is built using binary adders. A variety of computer arithmetic techniques can be used to implement a digital multiplier.



2*2 BIT BINARY MULTIPLIER





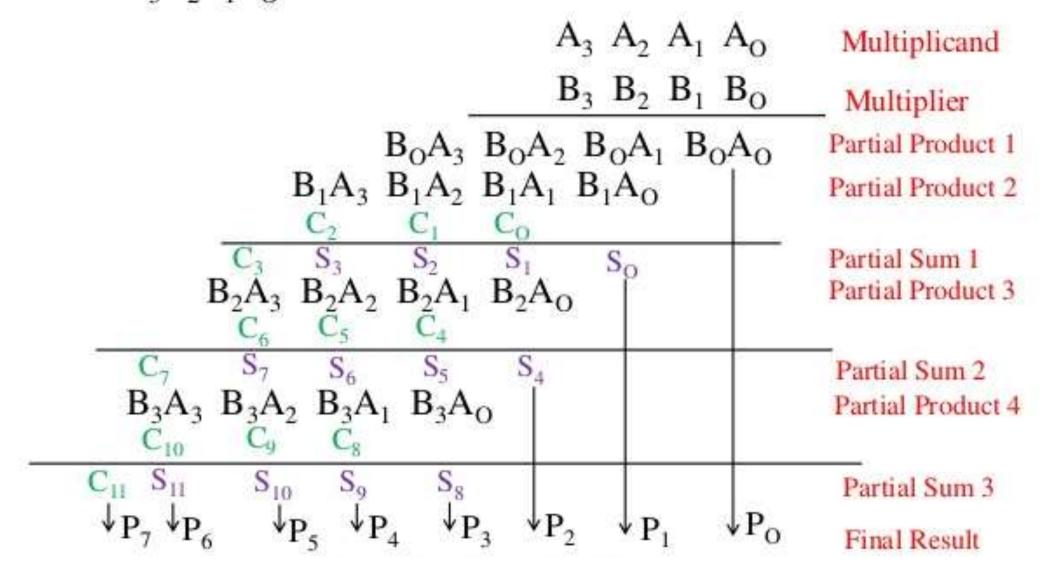


4*4 BIT BINARY MULTIPLIER



(iii) 4- Bit By 4-Bit Binary Multiplier:

It is a combinational circuit. This logic circuit is implemented to perform multiplication of two 4-bit binary numbers A= A₃A₂A₁A₀ and B=B₃B₂B₁B₀







THANK YOU