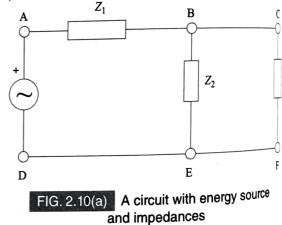


2.7 NORTON'S THEOREM

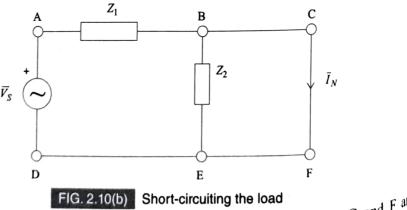
Norton's theorem states that "any two-terminal bilateral linear circuit consisting of e_{ne} sources and impedances can be replaced with a Norton's equivalent circuit with a_{sin} Norton current source \overline{I}_N in parallel with a Norton's impedance Z_N connected to a lo impedance Z_L ". Similar to Thevenin's theorem, Norton's theorem is also used to simplify the analysis of complex circuits.

The value of Norton's equivalent current \overline{I}_N can be calculated by shorting the load impedance Z_L . The Norton's equivalent impedance $Z_N \ \overline{\nu}_S$ is obtained by the procedure similar to that of calculating Thevenin's equivalent impedance Z_{Th} .

Consider the circuit shown in Fig. 2.10(a) to illustrate the application of Norton's theorem.



Step 1: To find Norton's current \overline{I}_N , short-circuit the load Z_L as shown in Fig. 2.10(b).



As the entire current flows through the short-circuited terminals C and F and currentflows through Z_2 , we have

$$\overline{I}_N = \frac{\overline{V}_S}{Z_1}$$

Step 2: To find Norton's equivalent impedance (Z_N)

$$Z_{N} = Z_{Th} = \frac{Z_{1}Z_{2}}{Z_{1} + Z_{2}}$$

Step 3: Norton's equivalent circuit with \overline{I}_N , Z_N and Z_L can be constructed as shown in Fig. 2.10(c).

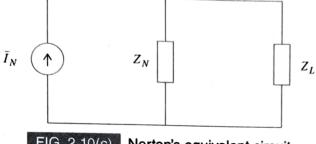


FIG. 2.10(c) Norton's equivalent circuit

It is important to note that using source transformation, Thevenin's equivalent circuit can be converted into Norton equivalent and vice versa as shown in Fig. 2.10(d).

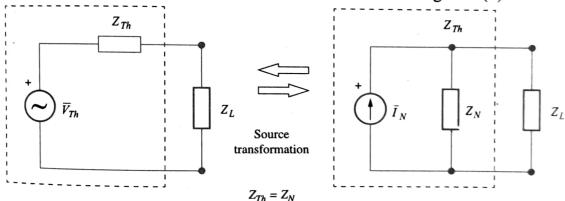
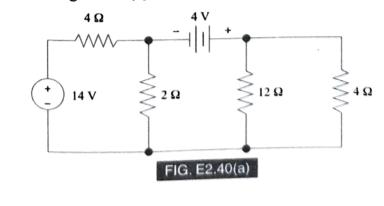


FIG. 2.10(d) Source transformation between Thevenin's equivalent circuit and Norton's equivalent circuit

If dependant sources are present in the circuit, the same procedure can be followed using Thevenin's theorem for solving problems.

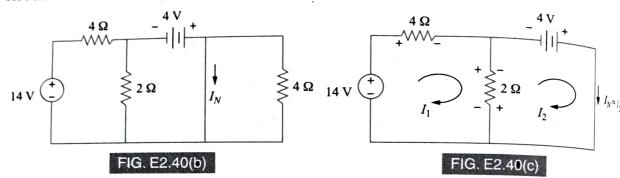
Example 2.40 Find the voltage drop across the 12Ω resistance using Norton's theorem for the circuit shown in Fig. E2.40(a).



Solution

^(a) To find Norton's current I_N .

Short the branch consisting of 12Ω resistor as shown in Fig. E2.40(b). Since the 4Ω resistor is in parallel with a short-circuited terminal, it is removed from the circuit as shown in Fig. E2.40(c).



Applying KVL to the circuit shown in Fig. E2.40(c), we get

For loop 1, $4I_1 + 2(I_1 - I_2) = 14$ i.e., $6I_1 - 2I_2 = 14$ (1) For loop 2, $2(I_2 - I_1) = 4$ i.e., $-2I_1 + 2I_2 = 4$ (2)

Upon solving Eqn.(1) and(2), we get

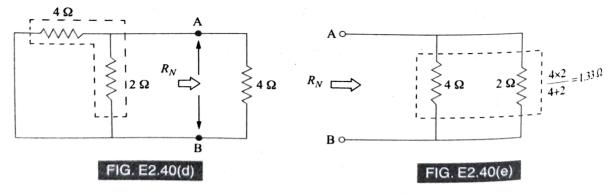
$$I_N = I_2 = 6.5 \,\mathrm{A}$$

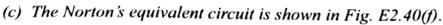
(b) To find R_{N} :

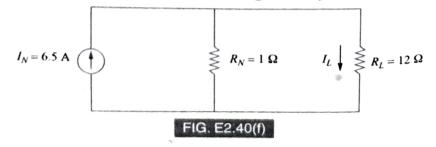
Open-circuit the 12 Ω resistor, and short-circuit the voltage source as shown in Fig. E2.40(d). The circuit is further simplified as shown in Fig. E2.40(e).

Therefore,

 $R_N = \frac{4 \times 1.333}{4 + 1.333} \approx 1\Omega$







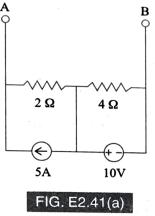
Therefore,

$$I_L = I_{12\,\Omega} = I_N \times \frac{R_N}{R_L + R_N}$$

= $6.5 \times \frac{1}{12 + 1} = 0.5 \text{ A}$

Voltage drop across the 12 Ω load resistor $V_L = I_L R_L = 0.5 \times 12 = 6$ V.

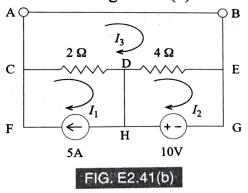
Example 2.41 Obtain the Norton's Equivalent for the circuit shown in Fig. E2.41(a).



Solution

(a) To find I_N :

Short-circuit the branch AB as shown in Fig. E2.41(b).



From Fig. E2.41(b), it is evident that

 $I_1 = 5 \text{ A}$ Applying KVL to the remaining loops, we get For the loop DEGHD,

$$4(I_2 - I_3) = 10$$

 $4I_2 - 4I_3 = 10$ For the loop ABEDCA

$$4(I_3 - I_2) + 2(I_3 - I_1) = 0$$

-2I_1 - 4I_2 + 6I_3 = 0
Upon solving Eqn. (1) and (2), we get

 $I_2 = 12.5 \text{ A} \text{ and } I_3 = 10 \text{ A}$

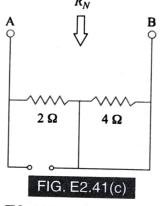
(2)

(1)

 $I_{N} = I_{3} = 10 \text{ A}$ Therefore,

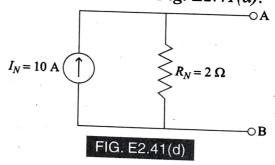
(b) To calculate R_N :

(b) To calculate R_N : Open-circuit the current source and short-circuit the voltage source as shown in Fig. E2.4 R_N

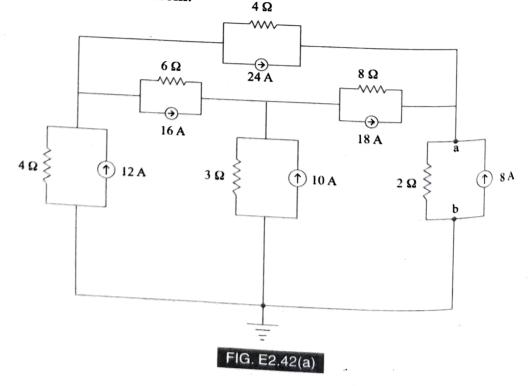


From the circuit shown in Fig. E2.41(c), it is evident that the 4Ω resistor is in part with the short circuit. Since the equivalent resistance of this parallel combination is z it can be removed.

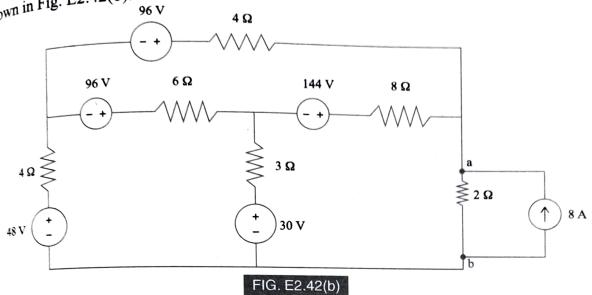
Therefore, $R_N = 2\Omega$ (c) The Norton's equivalent circuit is shown in Fig. E2.41(d).



Example 2.42 Determine the current through the 2Ω resistor in the circuit shown in E2.42(a), using Norton's theorem.

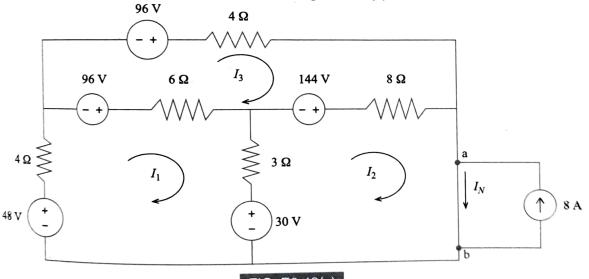


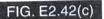
Solution Solution Solution transformation, the current sources are converted into voltage sources as Using = Fig E2.42(b). Solution shown in Fig. E2.42(b).



(a) To find I_N :

Short-circuit the load resistor 2Ω as shown in Fig. E2.42(c).





Applying KVL to this circuit, we get For loop 1,

$$13I_1 - 3I_2 - 6I_3 = 96 - 30 + 48$$

$$13I_1 - 3I_2 - 6I_3 = 114$$
 (1)

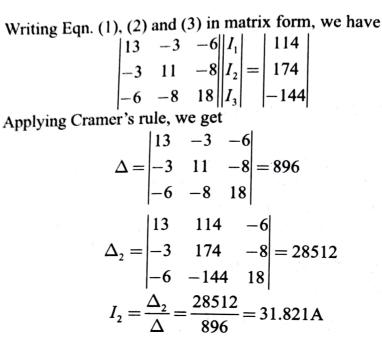
For loop 2,

^{For loop} 3,

 $-3I_1 + 11I_2 - 8I_3 = 144 + 30$ (2) $-3I_1 + 11I_2 - 8I_3 = 174$

$$-6I_1 - 8I_2 + 18I_3 = 96 - 144 - 96$$

$$-6I_1 - 8I_2 + 18I_3 = -144$$
 (3)



From Fig. E2.42(c), It is evident that $I_N = I_2 + 8 = 31.821 + 8 = 39.821$ A (b) To find R_N :

Short-circuit the voltage sources and remove the load R_L as shown in Fig. E2.42(d

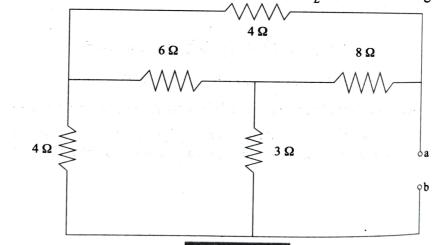
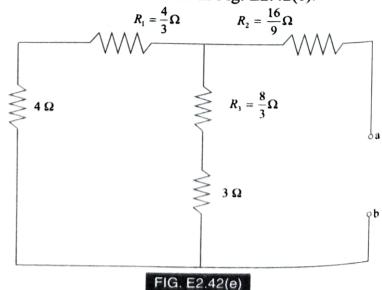


FIG. E2.42(d)





Here,

$$R_{1} = \frac{6+4+8}{6+4+8} = \frac{18}{18} = \frac{30}{3}$$
$$R_{2} = \frac{8\times4}{6+4+8} = \frac{32}{18} = \frac{16}{9}\Omega$$
$$R_{3} = \frac{6\times8}{6+4+8} = \frac{48}{18} = \frac{8}{3}\Omega$$

[/

6×4 24 4

and

Therefore,

$$R_N = R_{ab} = \left[\left(4 + \frac{4}{3} \right) \| \left(\frac{8}{3} + 3 \right) \right] + \frac{16}{9}$$
$$= \frac{5.333 \times 5.667}{5.333 + 5.667} + \frac{16}{9} = 4.525\Omega$$

(c) The Norton's equivalent circuit is shown in Fig. E2.42(f).

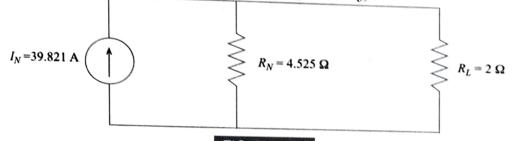


FIG. E2.42(f)

According to the current division rule,

R ----

$$I_{2\Omega} = \frac{39.821 \times 4.525}{4.525 + 2} = 27.6153 \text{ A}$$

Example 2.43 Obtain Norton's equivalent for the circuit shown in Fig. E2.43(a) across the terminal AB. 4Ω 6Ω

Solution

(a) To find I_{N} .

Short-circuit the load resistor R_x between the terminals AB, as shown in Fig. E2.43(b). Let the current flowing through the 4Ω resistor be I_x in the direction as shown in ^{Fig.} E2.43(b).

Applying KCL at node 'c', we get $4I = I + I_x$ 0r $I_{x} = 3I$ Applying KVL to the super mesh ECABDFE, we see

$$4I_x + 6I - 2I_x = 4$$

- 6(3I) + 6I = 4
- 12I = 4

